

## **WORKSHOPS, SEMINARS, FACULTY DEVELOPMENT PROGRAMS ATTENDED**

### **Dr.S.Sri Gowri**

- 1) 2-Day workshop on redesigning of curriculum in the context of outcome based learning conducted by JNTUK, Kakinada 24<sup>th</sup> -25<sup>th</sup> Feb 2013.
- 2) One Day work shop on communication for an inclusive class room, organized by SRK Institute of technology Enikepadu 25<sup>th</sup> March 2015.
- 3) A five day faculty Development program on Communication Skills organized by SRK Institute of technology Enikepadu 17<sup>th</sup> -21<sup>st</sup> Nov.2015.
- 4) 1<sup>st</sup> National Teachers Congress conducted by MIT School of Government, Pune, 23<sup>rd</sup> - 25<sup>th</sup> Sept. 2016.
- 5) National women's Parliament conducted by AP State Govtenment Amravati, 10<sup>th</sup> -12<sup>th</sup> Feb2017.
- 6) 2-Day workshop on Awareness Program on Establishing Incubation Centers conducted by JNTUK , Kakinada 15<sup>th</sup> -16<sup>th</sup> June 2017.
- 7) A one week Faculty Development Programme on "Long Term Evolution " at SRK Institute of Technology Dec. 2017
- 8) FDP Program in English for educators and Cambridge Business English Communication (BEC –Preliminary) held at SRKIT, Vijayawada from 9<sup>th</sup> OCT 2017 To 7<sup>th</sup> Nov 2017.

### **Ms. P.Subha Sree**

1. Two Week faculty Development programme on Recent Advances in "Microwave Engineering and its Applications" from 28.10.2013 to 09.11.2013.
2. IEEE National Workshop on Understanding 5G: Perspectives on future Technological Advancements in Wireless Communication held on 8<sup>th</sup> August-2015.
3. Two day Faculty Development program on real time signal and Image processing using Xilinx VIVADO and ZYNG -7000soc. in Collaboration with Core EL Technologies, Bangalore, Organized by department of ECE SRKT, during Sept. 11-12, 2015.

4. Five day faculty development program on Communication Skills Organized by department ECE in Association with English Department , SRK institute of Technology during 17<sup>th</sup> to 21<sup>st</sup> ,2015.
5. FDP Program in English for educators and Cambridge Business English Communication (BEC –Preliminary) held at SRKIT, Vijayawada from 9<sup>th</sup> OCT 2017 To 7<sup>th</sup> Nov 2017.
6. One Week FDP on Long Term Evolution and Advances in Mobile Communication.
7. A one week Faculty Development Programme on “Long Term Evolution ” at SRK Institute of Technology Dec. 2017.

**Mr. B.Ravi**

1. Two day workshop on circuit design & analysis using SPICE organized by the department of ECE in association with EeSTS\_research Hub-Pune from 02-02-2018 to 03-02-2018.
2. FDP Program in English for educators and Cambridge Business English Communication (BEC –Preliminary) held at SRKIT, Vijayawada from 9<sup>th</sup> OCT 2017 To 7<sup>th</sup> Nov 2017.

**Mr.Ratna Bhaskar**

- 1) A Two Week ISTE STTP on “Technical Communication” conducted by Indian Institute of Technology Bombay (IITB) from 8<sup>th</sup> Oct to 5<sup>th</sup> Dec 2015.
- 2) A One day Seminar on “Engineering Education & Research Seminar” at Kakinada on 19<sup>th</sup> Aug 2015 conducted by National Instruments India.
- 3) A IEEE National Workshop on “Understanding 5G: Perspectives on Future Technological Advancements in wireless Communication” on 8<sup>th</sup> Aug 2015.
- 4) A Two Day National Level Workshop, "Recent Trends In Wireless Communication Systems" UNDER TEQIP-II,S.C-12, in Gayatri Vidhya Parishad College of Engineering (A), Vishakapatnam on 25<sup>th</sup> to 26<sup>th</sup> March 2013.
- 5) The National Seminar on “Wireless Networking and Communications" WN&C 12 sponsored by APSCHE & UGC held during 27<sup>th</sup> – 28<sup>th</sup>, January 2012.
- 6) The National Institute of Technical Teachers Training & Research Quality Improvement Programme conducted by Ministry of Human Resource Development in SRK Institute of technology, Vijayawada on “ Instructional Design & Delivery Systems” from 1<sup>st</sup> to 5<sup>th</sup> June 2010

- 7) A Five Day FDP on “Digital Communications and FEC Coding Techniques” Organised by the Electronics & ICT Academy, ECE Department, National Institute of Technology, Warangal, during 11th April to 15th April 2016.
- 8) A One Day FDP on “Management and Communication skills” conducted by ICFAI Business School (IBS) on 24<sup>th</sup> July 2015.
- 9) A Five Day FDP on “Communication Skills” Organised by the Electronics and Communication Engineering in association with English Department, SRK Institute of Technology, Vijayawada during 17<sup>th</sup> -21<sup>st</sup> Nov, 2015.

**Ms.Shabeena Begam**

1. Participated in 2 day national symposium on “smart cities challenges and opportunities” on 05/05/2017 – 6/05/2017 .conducted by andhra pradesh state skil development corporation (apssdc), vijayawada.
2. Attended a one day guest lecture on “Teaching Methodology,Effective Communication Skills for Engineers and Educators“ conducted by department of English ,srkit,Vijayawada. On 04/05/2017.
3. Paricipated in 1 Week Faculty IMPROVEMENT Program On “EMBEDDED SYSTEMS” 29/11/2016 – 4/12/2016 .Conducted By ANDHRA PRADESH STATE SKIL DEVELOPMENT CORPORATION (APSSDC) And Associated With CoreEL Technologies. Hosted By Department Of Electronics And Communication Engineering, Godavari Institute Of Engineering And Technology.
4. A one day guest lecture on “CURRENT CHALLENGES IN SPEECH PROCESSING “conducted by department of electronics and communication engineering, srkit, Vijayawada. On 27/1/2016
5. A five day faculty development program on “COMMUNICATION SKILLS” organized by department of Electronics and Communication Engineering in association with English department ,SRK Institute of Technology,Vijayawada during November 17<sup>th</sup> - 20<sup>th</sup> 2015.
6. A two day training program on “MENTOR GRAPHICS“front and back end tools 7/10/2015 -8/10/2015. Conducted by Department of electronics and communication engineering, srkit, Vijayawada.

7. 1 week Faculty development program on “EMBEDDED SYSTEM DESIGN” 19/5/2015 – 23/5/2015. Conducted by Department of electronics and communication engineering, srkit, Vijayawada.
8. 1 month training program on ‘C’ language on 15/12/2014-6/01/2015. Conducted by Department of electronics and communication engineering, srkit, Vijayawada.
9. Participated in national seminar on “EMBEDDED SYSTEM DESIGN: Tools & Application “ organised by the department of computer science and engineering on 5/1/2015 in SIDDHARTHA ENGINEERING COLLEGE.
10. A two day work shop on “ADVANCED TRENDS IN EMBEDDED SYSTEMS,ATES-2014” 28 FEB-1 MARCH 2014. Conducted by Department of electronics and computer engineering & electrical and electronics engineering, PVPSIT ,kanuru-vijayawada .
11. A three day work shop on “ARM PROCESSOR“ 26/8/2014-28/8/2014. Conducted by Department of electronics and communication engineering, srkit, vijayawada.
12. A two day workshop on “ISSUES IN INTEGRATED CIRCUITS (IC’S) DESIGN” held during on 13/12/2013-14/12/2013 .Organized by the department of E.C.E , gudlalleru engineering college, gudlalleru,A.P
13. One day program on “WIRELESS SENSOR NETWORK“ 20/7/2013. Conducted by Department of electronics and communication engineering, srkit, Vijayawada.
14. Participated in the faculty development program on “VLSI and EMBEDDED SYSTEM DESIGN” organized by department of electronics and communication engineering, Gudlalleru engineering college -7- 8 july 2012.

**Mr.Vanam Naganjaneyulu-**

1. A Two Day Workshop in “International Student conference on the theme as Design a tablet computer” at International convention center (HICC), HYD –India 9-10 Jan 2012
2. A Two Day Faculty Development Program (FDP) on”VLSI& EMBEDDED SYSTEM DESIGN” IETE Vijayawada 7-8 July 2012
3. A Two Day Faculty Development Program (FDP) on Embedded System Design by SRK Institute of Technology, 19-23 may 2015

4. A Two Day Faculty Development Program (FDP) on Real time signal & image processing using Xilinx Vivado and Zynq-7000 SOC by Core EL Technologies, Bangalore 11-12 Sept, 2015
5. A Five-Day FDP on “Communication Skills” by SRK Institute Of Technology 17-21 November 2015
6. . A Two Day Workshop on Analog System Design using ASLK Pro at Dhanekula Institute of Engineering & Technology with Ed-Gate Technologies pvt ltd,16-17 Feb 2016
7. A Smooth conduction of the Workshop “overview on Latest Telecom Technologies” BSNL Krishna SSA for three days from 13-17 July 2016
8. A Six Day Faculty Improvement Program on Embedded Systems from 29 Nov-4 Dec 2016 Organized by APSSDC in association with core-EL Technology at GIET Rajahmundry.

**Mr.P.Raveendra-**

1. Two day workshop on circuit design & analysis using SPICE organized by the department of ECE in association with EeSTS\_research Hub-Pune from 02-02-2018 to 03-02-2018.
2. FDP Program in English for educators and Cambridge Business English Communication (BEC –Preliminary) held at SRKIT, Vijayawada from 9<sup>th</sup> OCT 2017 To 7<sup>th</sup> Nov 2017.

**Mr.P.Koteswara Rao-**

1. A one day guest lecture on “CURRENT CHALLENGES IN SPEECH PROCESSING “ conducted by department of electronics and communication engineering , SRKIT, Vijayawada. On 27/1/2016.
2. 2.A five day faculty development program on “COMMUNICATION SKILLS” organized by department of Electronics and Communication Engineering in association with English department ,SRK Institute of Technology, Vijayawada during November 17<sup>th</sup> -20<sup>th</sup> 2015.
3. A two day training program on “MENTOR GRAPHICS “ front and back end tools 7/10/2015-8/10/2015. Conducted by Department of electronics and communication engineering , Srkit, Vijayawada.

4. one week Faculty development program on “EMBEDDED SYSTEM DESIGN” 19/5/2015 – 23/5/2015. Conducted by Department of electronics and communication engineering ,SRKIT,Vijayawada.
5. one month training program on ‘C’ language on 15/12/2014-6/01/2015. Conducted by Department of electronics and communication engineering ,srkit,vijayawada.
6. A two day work shop on “ADVANCED TRENDS IN EMBEDDED SYSTEMS,ATES-2014” 28 FEB-1 MARCH 2014. Conducted by Department of electronics and computer engineering & electrical and electronics engineering,PVPSIT ,kanuru-vijayawada .
7. A three day work shop on “ARM PROCESSOR “ 26/8/2014-28/8/2014. Conducted by Department of electronics and communication engineering ,srkit,vijayawada.
8. One day program on “WIRELESS SENSOR NETWORK “ 20/7/2013. Conducted by Department of electronics and communication engineering ,SRKIT,Vijayawada.

**Mr. D.Ravi Tej-**

1. A Three day faculty development programme on “Design and Analysis of RF Antennas using HFSS” organized by department of ECE, LBRCE, during 29<sup>th</sup> Nov - 1<sup>st</sup> Dec 2016.
2. Work shop on “circuits and systems (CAS) / Electron Devices (ED) Research Forum” at KLEF deemed to be university, Vijayawada on 16<sup>th</sup> December 2017.

**Mr. K.Venkateswara Rao-**

1. One day program on “ADVANCED DESIGN CONCEPTS IN ELECTRONICS AND TELECOMMUNICATION “ on 22/6/2012. Conducted by FALCON ELECTRO-TEK PVT LTD.
2. One day program on “LAB WORKSTATION AND EMC/EMI MEASUREMENTS“ on 2/11/2012. Conducted by Agilent Technologies at PVP SIDDHARTHA Institute of Technology
3. A Two Day Workshop on “SATBOT WORKSHOP” conducted by tehnophilia Sysytems in association with Robotics & Computer Applications Institute of USA held at SRK Institute of Technology on 14-15 March 2014.
4. A three day work shop on “ARM PROCESSOR “ 26/8/2014-28/8/2014. Conducted by Department of electronics and communication engineering, srkit, Vijayawada

5. Three Day Faculty Development Program on “VLSI Design techniques using XILINX & MENTOR GRAPHICS EDA tools” at Andhra Loyala Institute of Engineering and Technology,Vijayawada, 23-25 JUNE 2016
6. A Two Day Workshop on Analog System Design using ASLK Pro at Dhanekula Institute of Engineering & Technology with EdGate Technologies pvt ltd,16-17 Feb 2016
7. One day program on “WIRELESS SENSOR NETWORK “ 20/7/2013. Conducted by Department of electronics and communication engineering ,srkit,vijayawada.
8. A two day training program on “MENTOR GRAPHICS “ front and back end tools 7/10/2015 -8/10/2015. Conducted by Department of electronics and communication engineering ,srkit,vijayawada.
9. Two-Day workshop on “Real Time Signal & Image Processing Using Xilinx-Vivado & Zinq-7000 SoC” by Core EL Technologies, Bangalore 11-12 Sept,2015
10. 1 week Faculty development program on “EMBEDDED SYSTEM DESIGN” 19/5/2015 – 23/5/1015.Conducted by Department of electronics and communication engineering ,srkit,Vijayawada
11. One-day FDP on “New Teaching Methods” at IBS ICFAI Business School on 24-July,2015, Vijayawada
12. A five day faculty development program on “COMMUNICATION SKILLS” organized by department of Electronics and Communication Engineering in association with English department ,SRK Institute of Technology,Vijayawada during November 17<sup>th</sup> - 20<sup>th</sup> 2015.
13. A Five Day Faculty Improvement Program on VLSI Design by Andhra Pradesh State Skill Development Corporation at SRK Institute,Vijayawada,15-19 Nov 2016.

#### **N.V.K MAHALAKSHMI**

1. A one day workshop on “Communication for an inclusive class room” conducted by department of English, SRKIT, Enikepadu, Vijayawada on 25<sup>th</sup> March 2017.
2. A Three day faculty development programme on “Design and Analysis of RF Antennas using HFSS” organized by department of ECE, LBRCE, during 29<sup>th</sup> Nov - 1<sup>st</sup> Dec 2016.

3. A one week quality improvement training programme on “Instructional Design and Delivery System” conducted by NITTT&R Chennai at SRKIT, Enikepadu, Vijayawada during 1<sup>st</sup> – 6<sup>th</sup> August 2016.
4. A one day guest lecture on “Current challenges in speech processing” conducted by department of ECE, SRKIT, Vijayawada on 27<sup>th</sup> January 2016.
5. A five day faculty development programme on “Communication Skills” organized by department of ECE in association with department of English, SRKIT, Enikepadu, Vijayawada during 17<sup>th</sup>-21<sup>st</sup> November 2015.
6. Two-Day Workshop on “Real Time Signal Processing and Image Processing using Xilinx Vivado & Zynq – 7000 SOC” conducted by department of ECE, SRKIT, Enikepadu, Vijayawada on 11<sup>th</sup> & 12<sup>th</sup> Sep 2015.
7. 1 month training program on ‘C’ language during 15/12/2014 - 6/01/2015 Conducted by department of ECE, SRKIT, Enikepadu, Vijayawada.
8. A Three day workshop on “ARM processors” conducted by department of ECE, SRKIT, Enikepadu, Vijayawada on 27<sup>th</sup>-29<sup>th</sup> August 2014.
9. AICTE sponsored two day workshop on “Atmospheric and weather Radar, GPS” organized by department of ECE, LBRCE on 28<sup>th</sup>- 29<sup>th</sup> march 2014.

**Ms. N.Mayuri**

A one day guest lecture on “CURRENT CHALLENGES IN SPEECH PROCESSING “ conducted by department of electronics and communication engineering , srkit,Vijayawada. On 27/1/2016.

1. A five day faculty development program on “COMMUNICATION SKILLS” organized by department of Electronics and Communication Engineering in association with English department ,SRK Institute of Technology, Vijayawada during November 17<sup>th</sup> - 20<sup>th</sup> 2015.
2. A two day training program on “MENTOR GRAPHICS “ front and back end tools 7/10/2015-8/10/2015. Conducted by Department of electronics and communication engineering , srkit, vijayawada.



3. one week Faculty development program on “EMBEDDED SYSTEM DESIGN” 19/5/2015 – 23/5/2015. Conducted by Department of electronics and communication engineering ,srkit,vijayawada.
4. one month training program on ‘C’ language on 15/12/2014-6/01/2015. Conducted by Department of electronics and communication engineering ,srkit,vijayawada.
5. A two day work shop on “ADVANCED TRENDS IN EMBEDDED SYSTEMS,ATES-2014” 28 FEB-1 MARCH 2014. Conducted by Department of electronics and computer engineering & electrical and electronics engineering,PVPSIT ,kanuru-vijayawada .
6. A three day work shop on “ARM PROCESSOR “ 26/8/2014-28/8/2014. Conducted by Department of electronics and communication engineering ,srkit,vijayawada.

**Ms. A.Venkata Prasanna Sarvari-**

1. Participated in Three day workshop on “Indo Universal Collaboration for Engineering Education (IUCEE) AP Chapter” conducted by APSSDC collaborated with IUCEE, K L University, Guntur. On 31<sup>st</sup> July to 2<sup>nd</sup> August 2017.
2. A three day faculty development program on “VLSI design techniques using XILINX & MENTOR GRAPHICS EDA tools” conducted by department of electronics and communication engineering, Andhra Loyola Institute of Engineering and Technology, Vijayawada. On 23<sup>rd</sup> to 25<sup>th</sup> June 2016.
3. A five day faculty improvement program on “VLSI Design” conducted by department of electronics and communication engineering, SRKIT, Vijayawada. On 15th to 19th Nov 2016.
4. A one day guest lecture on “CURRENT CHALLENGES IN SPEECH PROCESSING“ Conducted by department of electronics and communication engineering, srkit, Vijayawada on 27/1/2016.
5. A five day faculty development program on “COMMUNICATION SKILLS” organized by department of Electronics and Communication Engineering in association with English department ,SRK Institute of Technology, Vijayawada during November 17th - 20th 2015.

6. Participated in Two-Day Workshop on “Real Time Signal Processing and Image Processing using Xilinx Vivado & Zynq – 7000 SOC” conducted by SRKIT, Enikepadu, Vijayawada-08 on 11 & 12 Sep 2015.
7. One week Faculty development program on “Embedded system design” 19/5/2015 – 23/5/2015. Conducted by Department of electronics and communication engineering, srkit, Vijayawada.
8. 1 month training program on ‘C’ language on 15/12/2014-6/01/2015. Conducted by Department of electronics and communication engineering, srkit, Vijayawada.
9. A three day work shop on “ARM PROCESSOR“ 26/8/2014-28/8/2014. Conducted by Department of electronics and communication engineering, srkit, Vijayawada.
10. One day program on “WIRELESS SENSOR NETWORK“ 20/7/2013 Conducted by Department of electronics and communication engineering, srkit, Vijayawada.

**K. Devi Naga Nandini –**

- 1) A one week Faculty Development Programme on “Long Term Evolution ” at SRK Institute of Technology Dec. 2017
- 2) Participated in the one-week Faculty Development Program on “Long Term Evolution” organized by Department of ECE, SRK Institute of Technology, Vijayawada, India during December 2017.
- 3) Participated in the One Day Workshop on “Communication for an Inclusive Classroom” conducted by Department of English, SRK Institute of Technology, Vijayawada, India, on 25th March 2017.
- 4) Participated in National Women’s Parliament organized by Legislative Assembly, Andhra Pradesh at Amaravati, AP, India during 10-12, February 2017.
- 5) Participated in the Faculty Development Program on “Digital Communications and FEC Coding Techniques” organized by Electronics and ICT Academy, Department of ECE, National Institute of Technology, Warangal, India during 11th – 15th April, 2016.
- 6) Participated in a seminar on “MATLAB and Simulink for Engineering Education” conducted by Mathworks India in February 2016.

- 7) Participated in a five-day faculty development program on “Communication Skills” conducted by SRK Institute of Technology, Vijayawada, India, in December 2015.
- 8) Attended IEEE National workshop on “Understanding 5G: Perspectives on Future Technological Advancements in Wireless Communication” held at Potti Sriramulu Chalavadi Mallikarjuna Rao College of Engineering and Technology, Vijayawada, India, in August 2015.
- 9) Participated in a two-week ISTE workshop on Signals & Systems conducted by IIT Kharagpur, held at PVP Siddhartha Institute of Technology, Vijayawada, India, in January 2014.
- 10) Attended a one-day seminar on “Wireless Sensor Networks and Applications” organized by SRK Institute of Technology, Vijayawada, India, in July 2013.
- 11) Attended a two-day National Level Workshop on “Wireless Adhoc Networks” organized by GITAM University, Hyderabad, India, in May 2013.
- 12) Attended a two-day National Level Workshop on “Recent Trends in Wireless Communications Systems” organized by Gayatri Vidya Parishad College of Engineering, Visakhapatnam, India, in March, 2013.
- 13) Attended a three-day National Level Workshop on “Analog and Digital VLSI Circuits Design using Cadence EDA Tools” organized by ACE Engineering College, Ghatkesar, India, in June 2011.
- 14) Participated in the short-term course on “Instructional Design & Delivery Systems” conducted by National Institute of Technical Teachers Training and Research (NITTTR), Ministry of Human Resource Development, Govt. of India, Chennai, in June 2010.

**V.Sri Lakshmi-**

1. Participated in a seminar on “MATLAB and Simulink for Engineering Education” conducted by Mathworks India in February 2016.
2. Participated in faculty development program on “Signal Processing for Communications”
3. conducted by NIT, Warangal in January 2016.
4. Participated in a five-day faculty development program on “Communication Skills” conducted
5. by SRK Institute of Technology in December 2015.
6. Attended IEEE National workshop on “Understanding 5G: Perspectives on Future

7. Technological Advancements in Wireless Communication” held at Potti Sriramulu Chalavadi
8. Mallikarjuna Rao College of Engineering and Technology in August 2015.
9. Attended a one-day seminar on “Wireless Sensor Networks and Applications” organized by
10. SRK Institute of Technology, Vijayawada in July 2013.
11. Attended a two-day National Level Workshop on “Recent Trends in Wireless Communications
12. Systems” organized by Gayatri Vidya Parishad College of Engineering, Visakhapatnam, in
13. March 2013.
14. Participated in the short-term course on “Instructional Design & Delivery Systems” conducted by National Institute of Technical Teachers Training and Research (NITTTR), Ministry of Human Resource Development, Govt. of India, Chennai, in June 2010.

**Mr. CH.Siva Rajesh -**

1. Participated in the Training Programme on “INSTRUCTIONAL DESIGN AND DELIVERY SYSTEM”.
2. Participated in a two day workshop on “Real Time Signal & Image Processing Using Xilinx vivado and Zynq-7000 SOC” in collaboration with CoreEL Technologies.
3. Participated in a five day workshop on “Communication Skills” Organized by SRK Institute of Technology

**Ms. Sekharababu Velpula-**

1. Attended for faculty development program on” speech and image processing “organized by NITW in MVR college of engg.& tech on 10<sup>th</sup> to 15<sup>th</sup> April 2017
2. Participated in the two day National Seminar on” Real Time Signal & Image Processing Using Xilinx Vivado and Zynq-7000 SOC” on 11<sup>th</sup> and 12<sup>th</sup> September 2016 at SRK Institute of Technology, Vijayawada
3. Participated in the two day National Seminar on” Advances in Image Processing” on 24<sup>th</sup> and 25<sup>th</sup> June 2016 at VVIT Nambur..
4. Attended the complementary seminar “Math Works India” on 5<sup>th</sup> feb 2016.
5. Participated in the one week faculty development program on ”Recent Trends in Systems and Signal Processing (RTSSP-2014).conducted at LBRCE mylavarm.

6. Participated in “green Energy Technology Center” poster presentation conducted at LBRCE mylavaram held on July 2010.
7. Participated in International Conference based Nano Technology and submitted paper on “human and environmental risks of nano technology and nano materials “on 12<sup>th</sup> sep.2008 conducted at JNTU ANANTAPUR

**Ms.B. Anitha Pravalli-**

1. Two-Day National Convention on “Recent Advances in Microwave, Antennas and Communications” in association with The Institution of Engineers.
2. Two-Day National Seminar on “ Smart Antenna Technologies for Future Wireless Systems: Trends & Challenges” Sponsored by DST
3. Two-Day workshop on “Antenna Simulation using ANSYS HFSS Tools” in collaboration with Entuple Technologies, Bangalore.
4. Two-Day seminar on “ Foundation of Random Processes and Video Analytics Applications” Sponsored by SAGTE.
5. Two day workshop on circuit design & analysis using SPICE organized by the department of ECE in associationwith EeSTS\_research Hub-Pune from 02-02-2018 to 03-02-2018.
6. A one week Faculty Development Programme on “Long Term Evolution ” at SRK Institute of Technology Dec. 2017.

**Mr.G.Suryaprakash-**

1. A one week Faculty Development Programme on “Long Term Evolution ” at SRK Institute of Technology Dec. 2017.

**Mr.B.S.S.Tejesh-**

1. Two day workshop on circuit design & analysis using SPICE organized by the department of ECE in associationwith EeSTS\_research Hub-Pune from 02-02-2018 to 03-02-2018.

**Ms. S.Neeraja-**

1. Two day workshop on circuit design & analysis using SPICE organized by the department of ECE in associationwith EeSTS\_research Hub-Pune from 02-02-2018 to 03-02-2018.