

SRK INSTITUTE OF TECHNOLOGY
Enikepadu, Vijayawada 521108
Approved by AICTE, Affiliated to JNTUK, Kakinada
(ISO 9001:2008 Certified Institution)
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Date: 01.10.2015

PROGRAM REPORT

Visited:

Kumar Pumps and Motors, Tenali

No. of Students: 49

Faculty Coordinators:

- Mr. S.NAGESWARA RAO, Assistant Professor
- Mrs. T.V. MAHA LAKSHMI, Assistant Professor
- Mrs. T.VISHNU PRIYA Assistant Professor

On receiving the letter of permission from Kumar pumps and motors, Tenali. Students with three faculty members went on an industrial visit to Kumar pumps on 30-09-2015. We all assembled at the college at 9:00a.m and left the college in a college bus. We reached the Kumar pumps at 10.00a.m. the Kumar pumps member G.N.Malleswara Rao received us at the entrance and gave a brief introduction about the process of manufacturing of reciprocating pumps and motors. Bore wells and they're working in detail.

Visited sections in the company:

- Preparation of moldings and casting
- Preparation of windings
- Assembling and Testing


PURPOSE OF VISIT:

Our main purpose for this visit is to be familiar with industrial environment and to get practical knowledge of different Pumps & Motors.

OUTCOME:

Students get familiar with motor maintenance, material to be used in motor like conductor, insulators, different fabrication motor etc.


HOD


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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Date: 18.09.2015

PROGRAM REPORT

Name of the Program : Two-day Workshop on "PCB DESIGN".

Date / Duration : 14. 09. 2015 to 15. 09. 2015 (2 - Days)

Resource Person : Mr. Ch. Kishore, Director, Microlink Information Technologies

Name of Coordinator : Mr. B. Nava Jeevan Reddy, Assistant Professor, EEE

Number of Participants: 58

Program Objectives : The objective of this program is to introduce participants to the fundamentals of Circuits Design on Printed Circuit Board.

Topics Covered : The following topics were discussed in this workshop

- Introduction to the Basic Electronics Components and Soldering Techniques
- Circuit layout Design using Spice Tool
- Testing of the Implemented Circuit



Program Outcome : The workshop is intended to facilitate the students to acquire Knowledge and information in the design of various Electronic Circuits on Printed Circuit Boards.

GJR
Coordinator

S. Sri Gowd
HOD/ Date

Chandrababu 18/9/15
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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

**LIST OF INTERNSHIP PROGRAM ATTENDEES
A.Y. 2015-16**

S.NO	PROGRAM	ROLL NO	NAME OF THE STUDENT	TOPIC	DURATION	DATE	NAME OF THE ORGANIZATION
1	B. Tech	13X41A0203	P.Rashiuddin	Embedded Systems Design with Microcontroller & Programming	1Month	02.05.2016 to 31.05.2016	Microlink peripheral controls(P)Ltd
2	B. Tech	13X41A0215	K.Naga Vishnu	Embedded Systems Design with Microcontroller & Programming	1Month	02.05.2016 to 31.05.2016	Microlink peripheral controls(P)Ltd
3	B. Tech	13X41A0229	P. V.Narasimha Rao	Embedded Systems Design with Microcontroller & Programming	1Month	02.05.2016 to 31.05.2016	Microlink peripheral controls(P)Ltd
4	B. Tech	13X41A0241	P.Surendar Reddy	Embedded Systems Design with Microcontroller & Programming	1Month	02.05.2016 to 31.05.2016	Microlink peripheral controls(P)Ltd
5	B. Tech	14X45A0213	N.Santosh Kumar	Embedded Systems Design with Microcontroller & Programming	1Month	02.05.2016 to 31.05.2016	Microlink peripheral controls(P)Ltd

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S.Svi Gowri
HOD

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DEPARTMENT OF ELECTRONICS AND COMMUNICATIONS ENGINEERING


Date: 18.09.2015

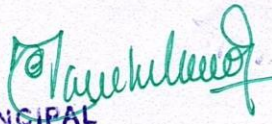
PROGRAM REPORT

- Name of the Program** : Two-days Workshop on “PCB DESIGN”.
- Date / Duration** : 14. 09. 2015 to 15. 09. 2015 (2 - Days)
- Resource Person** : Mr. Ch. Kishore, Director, Microlink Information Technologies
- Name of Coordinator** : Mr. B. Ravi, Assistant Professor, ECE
- Number of Participants:** 114
- Program Objectives** : The objective of this program is to introduce participants to the fundamentals of Circuits Design on Printed Circuit Board.
- Topics Covered** : The following topics were discussed in this workshop
- Introduction to the Basic Electronics Components and Soldering Techniques
 - Circuit layout Design using Spice Tool
 - Testing of the Implemented Circuit

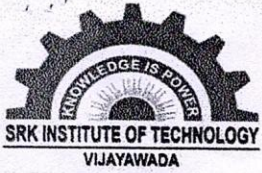


Program Outcome : The workshop is intended to facilitate the students to acquire Knowledge and information in the design of various Electronic Circuits on Printed Circuit Boards.


Coordinator


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S. Sri Gowd
HOD/ Date
18/9/2015



Program Report

Date: 27-10-2015

Duration: 1 Week (19-10-2015 to 25-10-2015)

The Department of Electronics and Communication Engineering and Micro Link jointly organized a Five-day Faculty Development Program on “**Embedded System Design**” from 19-10-2015 to 25-10-2015. The Resource Person for the Session is Mr. **Govinda Rao**, from Microlinks.

On Day-1, Mr. **Govinda Rao**, started his session by giving introduction to 8051 micro controller. He clearly explained the basic concepts of 8051 micro controller, how it works, how to use this 8051 micro controller in different projects. He clearly explained the basic concepts of 8051 micro controller.

On Day-2, Mr. **Govinda Rao**, clearly explained how to interface 8051 micro controller with LCD and afterwards the faculty were also did this interfacing in the lab. In the afternoon session Mr, **Govinda Rao**, also explained keyboard interfacing with the 8051 micro controller. Afterwards the faculty were also did that interfacing in the lab. After that Mr, **Govinda Rao**, also explained interfacing of temperature sensor and LCD with 8051 micro controller, afterwards the faculty were also did that interfacing in the lab.

On Day-3, Mr. **S Srinivasa Rao**, clearly explained the stepper motor interfacing with 8051 micro controller. Afterwards the faculty were also did that interfacing in the lab. In afternoon session, Mr. **S Srinivasa Rao**, also clearly explained the interfacing of external memory with 8051 micro controller, afterwards the faculty were also did that interfacing in the lab.

On Day-4, Mr. **S Srinivasa Rao**, clearly explained the interfacing of 7 segment display with 8051 micro controller, afterwards the faculty were also did that interfacing in the lab session.


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On Day-5, Mr. **Govinda Rao**, clearly explained the interfacing of updown counter design using 7 segment display and 8051 micro controller, afterwards the faculty were also did that in the lab session.

On Day-6, Mr. **Govinda Rao**, clearly explained the interfacing of GPS with 8051 micro controller, afterwards the faculty were also did that interfacing in the lab session.

On Day-7, Mr. **S Srinivasa Rao**, clearly explained the interfacing of DC Motor with 8051 micro controller.

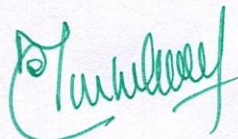
Mr. **Govinda Rao**, has excellent knowledge in Embedded System Design, especially he has good knowledge of 8051 micro controller, he can interfacing any relevant component to this controller, also he has good programming skills, and he delivered excellent knowledge in the sessions. The faculty are also very much satisfied with his content. The faculty are learned so many techniques from Mr. **Govinda Rao**. All the faculty gave an excellent feedback to the faculty development program and also to the Mr. **Govinda Rao** and Mr. **S Srinivasa Rao**.

Course Objectives:

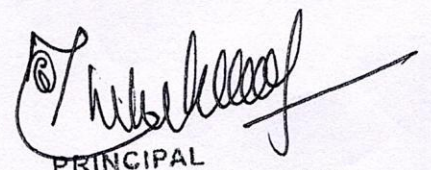
1. Understanding how to interface 8051 micro controller with LCD.
2. Understanding how to interface 8051 micro controller with keyboard.
3. Understanding how to interface 8051 micro controller with temperature sensor and LCD.
4. Understanding how to interface 8051 micro controller with stepper motor.
5. Understanding how to interface 8051 micro controller with external memory interface.
6. Understanding how to interface 8051 micro controller with 7 segment display.
7. Understanding how to interface 8051 micro controller with updown counter design using 7 segment displays.
8. Understanding how to interface 8051 micro controller with GPS

Course Outcomes:

1. Understood how to interface 8051 micro controller with LCD.
2. Understood how to interface 8051 micro controller with keyboard.
3. Understood how to interface 8051 micro controller with temperature sensor and LCD.
4. Understood how to interface 8051 micro controller with stepper motor.
5. Understood how to interface 8051 micro controller with external memory interface.



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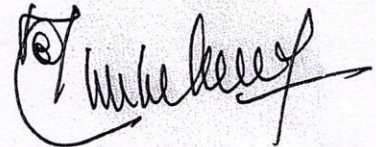


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- Cnb
6. Understood how to interface 8051 micro controller with 7 segment display.
 7. Understood how to interface 8051 micro controller with updown counter design using 7 segment displays.
 8. Understood how to interface 8051 micro controller with GPS

Md-Shabreena
Co-ordinator

S. Sri Gowri
Head of the Department



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Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: P. Subhasri
 Designation: Assist Professor

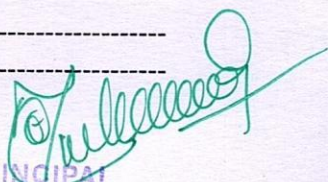
Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

Rating Score: Excellent (5), Very Good (4), Good (3), Average (2), Below Average (1)

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2	Did the presenter answer your question, if any?	✓				
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6	Did the presenter maintain control of the discussion points?	✓				
7	Did the presenter use visual aids for reinforcement of discussion points?	✓				
8	Did the presenter hold your interest?	✓				
9	How do you rate the presenter's ability to manage time?	✓				
10	How do you rate the support level provided in the lab (if applicable)	✓				

Any suggestions : -----


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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
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Feedback Form

Participant Name: T. Vishnu Priya
 Designation: Asst. Professor

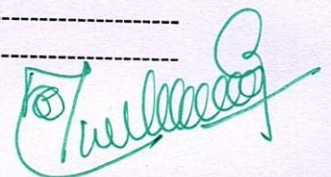
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Any suggestions : -----



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SRK Institute of Technology
Department of Electronics and Communication Engineering
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Feedback Form

Participant Name: P. Ratna Bhaskar
 Designation: Assistant Professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

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Any suggestions : -----

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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
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Feedback Form

Participant Name: B. Rawat
 Designation: Assistant Professor

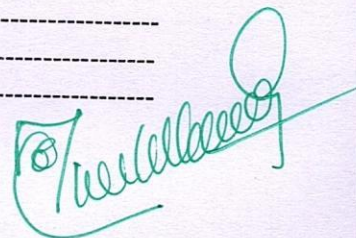
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Any suggestions : -----



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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
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Feedback Form

Participant Name: K. Manjiga Priyadarshi
 Designation: Asst. Professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

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Any suggestions : -----

(Signature)

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Department of Electronics and Communication Engineering
Faculty Development Program on
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Feedback Form

Participant Name: Y. Rakesh
 Designation: Assistant professor

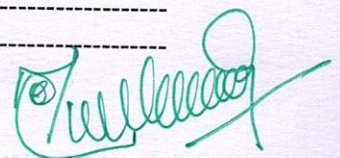
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Any suggestions : -----



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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: K. prasuna
 Designation: Asst. professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

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Any suggestions : -----

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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
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Feedback Form

Participant Name: K. MURALI
 Designation: ASSISTANT PROFESSOR.

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

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Any suggestions : -----

(Handwritten Signature)

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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: M. Sadanya
 Designation: Asst. Professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

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Any suggestions : -----

M. Sadanya

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: M. Naga mami.
 Designation: Asst. professor.

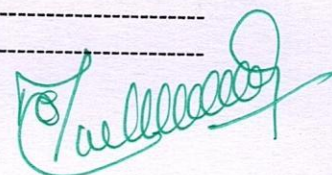
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Any suggestions : -----



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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: Ms. shabana Bhegam
 Designation: Assistant Professor

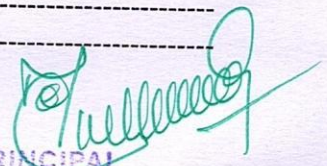
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Any suggestions : -----



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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: V. Nagarajeyulu
 Designation: Assistant Professor

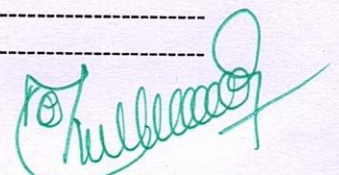
Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

Rating Score: Excellent (5), Very Good (4), Good (3), Average (2), Below Average (1)

S.NO	Description	5	4	3	2	1
1	How would you describe the presenter's knowledge of the subject matter?	✓				
2	Did the presenter answer your question, if any?	✓				
3	How did you find the presenter's Articulation and style of delivery?	✓				
4	Were the concepts, principles and techniques explained in an understandable manner?	✓				
5	Did the presenter invite and encourage individual participation?	✓				
6	Did the presenter maintain control of the discussion points?	✓				
7	Did the presenter use visual aids for reinforcement of discussion points?	✓				
8	Did the presenter hold your interest?	✓				
9	How do you rate the presenter's ability to manage time?					
10	How do you rate the support level provided in the lab (if applicable)	✓				

Any suggestions : -----


 PRINCIPAL

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: P. N. V. Siva Kumar
 Designation: Assistant Professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

Rating Score: Excellent (5), Very Good (4), Good (3), Average (2), Below Average (1)

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1	How would you describe the presenter's knowledge of the subject matter?	/				
2	Did the presenter answer your question, if any?	/				
3	How did you find the presenter's Articulation and style of delivery?	/				
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7	Did the presenter use visual aids for reinforcement of discussion points?	/				
8	Did the presenter hold your interest?	/				
9	How do you rate the presenter's ability to manage time?	/				
10	How do you rate the support level provided in the lab (if applicable)	/				

Any suggestions : -----


 PRINCIPAL

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: K. Raja Rajeswari
 Designation: Asst. professor

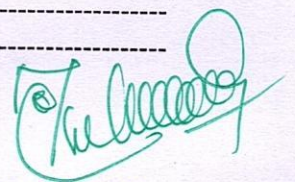
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8	Did the presenter hold your interest?	✓				
9	How do you rate the presenter's ability to manage time?	✓				
10	How do you rate the support level provided in the lab (if applicable)	✓				

Any suggestions : -----



PRINCIPAL
SRK Institute of Technology
ENIKEPADU, VIJAYAWADA-521 108

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: B. Jyothi
 Designation: Assistant Professor

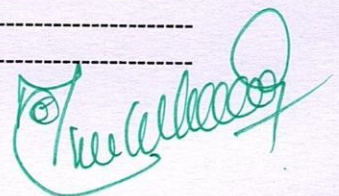
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8	Did the presenter hold your interest?	✓				
9	How do you rate the presenter's ability to manage time?	✓				
10	How do you rate the support level provided in the lab (if applicable)	✓				

Any suggestions : -----



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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: P. Silpa
 Designation: Asst Professor

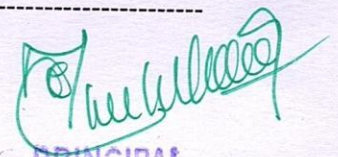
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Any suggestions : -----



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SRK Institute of Technology
 ENIKEPADU, VIJAYAWADA-521 108

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: Y. Srisha
 Designation: Asst. professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

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Any suggestions : -----

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SRK Institute of Technology
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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: P. Ravindra
 Designation: Assistant professor

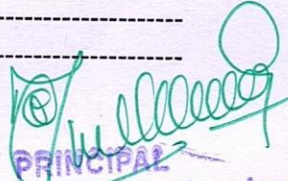
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Any suggestions : -----


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SRK Institute of Technology
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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: P. Robinson Reddy
 Designation: Assistant professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

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8	Did the presenter hold your interest?	✓				
9	How do you rate the presenter's ability to manage time?	✓				
10	How do you rate the support level provided in the lab (if applicable)	✓				

Any suggestions : -----

(Handwritten Signature)

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SRK Institute of Technology
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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: D. Ravi Tej
 Designation: Assistant professor.

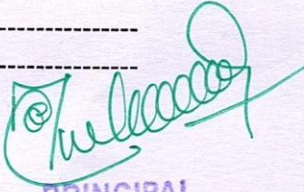
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8	Did the presenter hold your interest?	✓				
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10	How do you rate the support level provided in the lab (if applicable)	✓				

Any suggestions : -----


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 SRK Institute of Technology
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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: K. Venkateswar Rao
 Designation: Assistant Professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

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8	Did the presenter hold your interest?	✓				
9	How do you rate the presenter's ability to manage time?	✓				
10	How do you rate the support level provided in the lab (if applicable)	✓				

Any suggestions : -----

PRINCIPAL
SRK Institute of Technology
 ENIKEPADU, VIJAYAWADA-521 103

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: N. Mayuri
 Designation: Assistant Professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

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8	Did the presenter hold your interest?	✓				
9	How do you rate the presenter's ability to manage time?	✓				
10	How do you rate the support level provided in the lab (if applicable)	✓				

Any suggestions : -----

(Handwritten signature in green ink)

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: A. Varaja
 Designation: Assistant professor

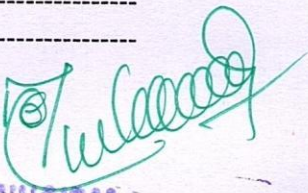
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Any suggestions : -----



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 SRK Institute of Technology
 ENIKEPADU, VIJAYAWADA-521 108

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: A.V.P. Sarvani
 Designation: Asst. Professor

Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

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Any suggestions : -----

(Signature)
PRINCIPAL
 SRK Institute of Technology
 ENIKEPADU, VILAYAWADA-521 108

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: K. Devi Naga Nandhini
 Designation: Asst. professor

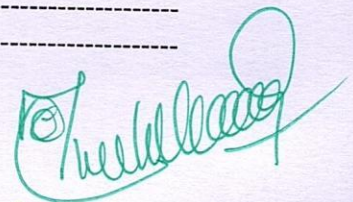
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Any suggestions : -----



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SRK Institute of Technology
 ENIKEPADU, VIJAYAWADA-521 108

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: V. Sri Lakshmi
 Designation: Asst. Professor

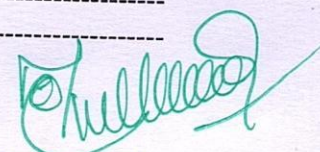
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Any suggestions : -----



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SRK Institute of Technology
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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: Y. Pramodhini
 Designation: Assistant Professor.

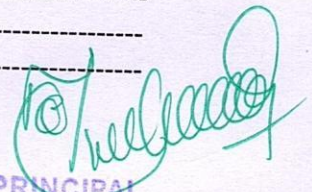
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Any suggestions : -----



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SRK Institute of Technology
 ENIKEPADU, VIJAYAWADA-521 108

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: Ch. Siva Dajesh
 Designation: Assistant Professor

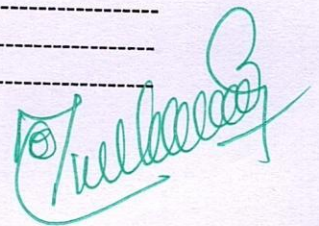
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Any suggestions : -----



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SRK Institute of Technology
ENIKEPADU, VIJAYAWADA-521 108

SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: N. V. K. Maha lakshmi.
 Designation: ASst professor.

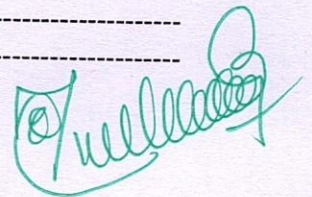
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Any suggestions : -----



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SRK Institute of Technology
Department of Electronics and Communication Engineering
Faculty Development Program on
“Embedded System Design”

Feedback Form

Participant Name: CH Vijaya Sekhan Babu
 Designation: Assistant Professor

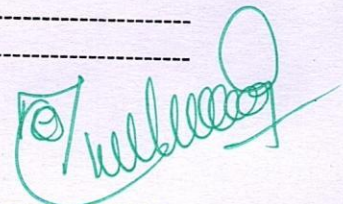
Kindly give your feedback on different aspects of the training program which you have completed. Your response will be useful in improving the quality of our future program.

Please indicate your level of satisfaction with the following statement by choosing between 1 and 5, 1 is the lowest and 5 is the highest score.

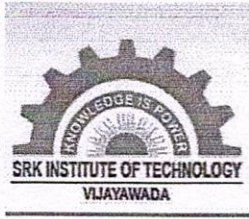
Rating Score: Excellent (5), Very Good (4), Good (3), Average (2), Below Average (1)

S.NO	Description	5	4	3	2	1
1	How would you describe the presenter's knowledge of the subject matter?	✓				
2	Did the presenter answer your question, if any?	✓				
3	How did you find the presenter's Articulation and style of delivery?	✓				
4	Were the concepts, principles and techniques explained in an understandable manner?	✓				
5	Did the presenter invite and encourage individual participation?	✓				
6	Did the presenter maintain control of the discussion points?	✓				
7	Did the presenter use visual aids for reinforcement of discussion points?	✓				
8	Did the presenter hold your interest?	✓				
9	How do you rate the presenter's ability to manage time?	✓				
10	How do you rate the support level provided in the lab (if applicable)	✓				

Any suggestions : -----



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING

Academic year: 2015-16

Year/Sem: IV/II

List of IV/IV B. Tech, ECE Students Attended for 30-days Internship Training on "Embedded Systems Design with Microcontroller & Programming" at Microlink Information Technologies, Vijayawada from 02.11.2015 to 04.12.2015.

S. NO	ROLL NUMBER	NAME OF THE STUDENT
1	12X41A0404	ABDUL RESHMA
2	12X41A0416	GURINDAPALLI SANDEEP
3	12X41A0435	MANGALAGIRI VIRAJITHA
4	12X41A0445	PONUGUMATI BHARGAVI
5	12X41A0454	SWARNA NITISHA
6	12X41A0474	JONNALAGADDA KEERTHI
7	12X41A0484	MARRIVADA N V SARATH
8	12X41A0495	PINNABOINA SIVANI
9	12X41A04A3	SK. AYESHA
10	13X45A0410	G. RAJESH

S. Sri Gowri
HOD

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING**

Academic Year: 2015-16

Year/Sem: III/II

List of III/IV B. Tech, ECE Students Attended for 30-days Internship Training on "Embedded Systems Design with Microcontroller & Programming" at Microlink Information Technologies, Vijayawada from 02.05.2016 to 31.05.2016.

S. NO	ROLL NUMBER	NAME OF THE STUDENT
1	13X41A0404	B NARENDRA KUMAR
2	13X41A0414	M LAVANYA NAGA SRI
3	13X41A0435	L VINAY DATTA
4	13X41A0448	V RAMU
5	13X41A0472	B SRI SAI CHAITANYA
6	13X41A0482	Y NAGA LAKSHMI
7	14X45A0410	K BHAGYA RAJ
8	14X45A0413	G ANUSHA
9	14X45A0424	G JEEVAN KUMAR

S. Sn'Gown
HOD

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Department of Computer Science and Engineering

Date: 10-06-2016

Report

Name of the Program: "Microsoft Edu-Cloud services".

Date: 01-03-2016 to 30-06-2016

Timings: 03:00 AM to 4:30 PM

The students of III/II B.Tech attended the certification program from 01-03-2016 to 30-06-2016 at NecX Private Limited, Hyderabad. The students got awareness of Microsoft Edu-Cloud services and successfully completed the certification program and received certificates.

Program Coordinator

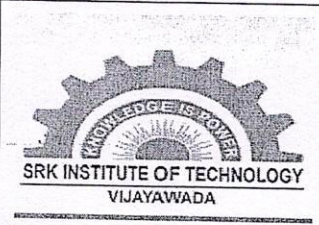
D. Satish
HOD/Date 10/6

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Department of Computer Science and Engineering

Date: 31-05-2016

Report

Name of the Program: "Microsoft Edu-Cloud services".

Date: 02-05-2016 to 29-05-2016

Timings: 09:00 AM to 4:30 PM

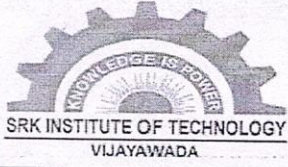
The students of III/II B.Tech attended the internship program from 02-05-2016 to 29-05-2016 at NecX Private Limited, Hyderabad. The students got awareness of Microsoft Cloud Services and successfully completed the internship program and received certificates.

Program Coordinator

HOD/Date 31/05/16

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Department of Computer Science and Engineering

Academic Year: 2015-2016

Year/ Sem: III / II

List of Students completed Summer Internship Program on "Microsoft Edu-Cloud services".

S.NO	ROLL NUMBER	NAME OF THE STUDENT	SIGNATURE
1	13X41A05A5	SABBINENI ROHITHA	S. Rohitha
2	13X41A0530	NAJANI KEERTHI	Keerthi
3	13X41A0533	NUTHAKKI MANOGNA	Manogna.
4	13X41A0523	KOVI JAGRUTHNATH	K. Jagruthnath
5	13X41A0553	UHA SRI KONA	U. S. Kona

Program Coordinator

HOD

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Department of Computer Science and Engineering

Program Report

Date: 11-01-2016

Name of the Program : "MTA-Networking Fundamentals"
Duration : 24-08-2015 to 03-10-2015
Resource Person : Mrs A Radhika and Dr D Haritha
Collaborating Agency : Microsoft and NEcX Pvt Ltd
Topics Covered : Understanding Local Area Networking
Defining Networks with the OSI Model
Understanding Internet Protocol
Implementing TCP/IP in the Command Line
Working with networking services
Understanding Wide Area Networks
Network Infrastructure and Network security

Program Outcome:

- The student should able to develop LAN Networking
- The student should develop the TCP/IP Suite
- The students should able to do networking services

No. Of students completed/enrolled: 99/ 99

Radhika
Instructor

D. Haritha
HOD

[Signature]

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Program Report

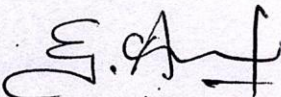
Date: 11-01-2016

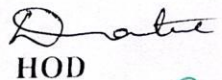
Name of the Program : "MTA-Networking Fundamentals"
Duration : 24-08-2015 to 03-10-2015
Resource Person : Mr G Appa Rao, Asst Professor, Phone No: 8099388998
Collaborating Agency : Microsoft and NEcX Pvt Ltd
Topics Covered :
Understanding Local Area Networking
Defining Networks with the OSI Model
Understanding Internet Protocol
Implementing TCP/IP in the Command Line
Working with networking services
Understanding Wide Area Networks
Network Infrastructure and Network security

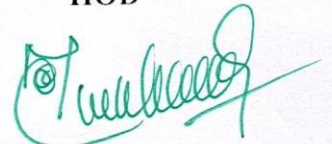
Program Outcome:

- The student should be able to develop LAN Networking
- The student should develop the TCP/IP Suite
- The students should be able to do networking services

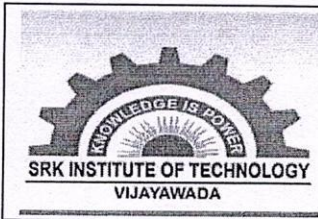
No. Of students completed/enrolled: 65/ 65


Instructor


HOD



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Department of Computer Science and Engineering

Program Report

Date: 11-01-2016

Name of the Program : "MTA-Data Fundamentals"
Duration : 24-08-2015 to 03-10-2015
Resource Person : Mr N Sudhakar Reddy
Collaborating Agency : Microsoft and NEcX Pvt Ltd
Topics Covered : Core Data Base Concepts
Data Base Objects
Manipulates Data
Data Storage
Administer a Data Base

Program Outcome:

- The students Got depth knowledge on data base management concepts and their applications

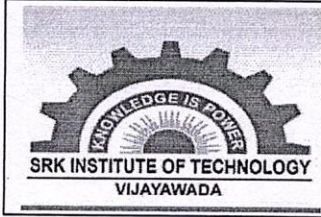
No. Of students completed/enrolled: 26/ 26

[Signature]
Instructor

[Signature]
HOD

[Signature]

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Department of Master of Computer Applications

Program Report

Date: 11-01-2016

Name of the Program : "MTA-Networking Fundamentals"
Duration : 24-08-2015 to 03-10-2015
Resource Person : Ms. M.Anitha, anitha3harshak@gmail.com,9912683450
Collaborating Agency : Microsoft and NEcX Pvt Ltd
Topics Covered : Understanding Local Area Networking
Defining Networks with the OSI Model
Understanding Internet Protocol
Implementing TCP/IP in the Command Line
Working with networking services
Understanding Wide Area Networks
Network Infrastructure and Network security

Program Outcome:

- The student should able to develop LAN Networking
- The student should develop the TCP/IP Suite
- The students should able to do networking services

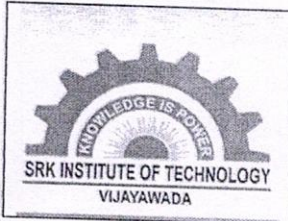
No. Of students completed/enrolled: 21/21

M. Anitha
Instructor

Anitha
HOD

[Signature]

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DEPARTMENT OF MECHANICAL ENGINEERING

Date: 15-09-2015

Program Report

Name of the Event: One week ADD ON program on "Siemens Product Lifecycle Management (PLM) Software Training"

Date / Duration: 07-09-2015 to 12-09-2015

Resource Person:

Mr.G Durga prasad, Assistant Professor, SRKIT-Vijayawada
contact no. +91-9490911717


Name of Coordinator: Mr. G. Durga Prasad, Asst. Professor, Mechanical Engineering

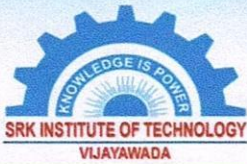
Number of students Attended: 32

Objective of the Event:

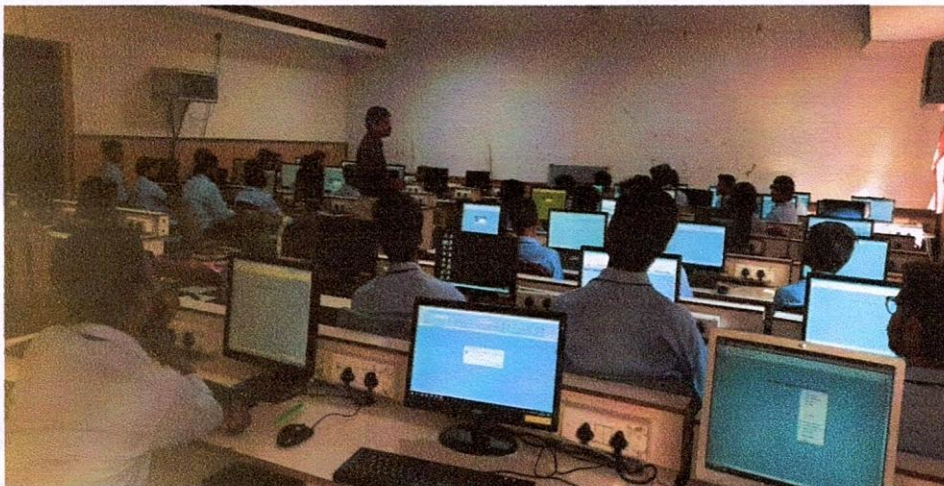
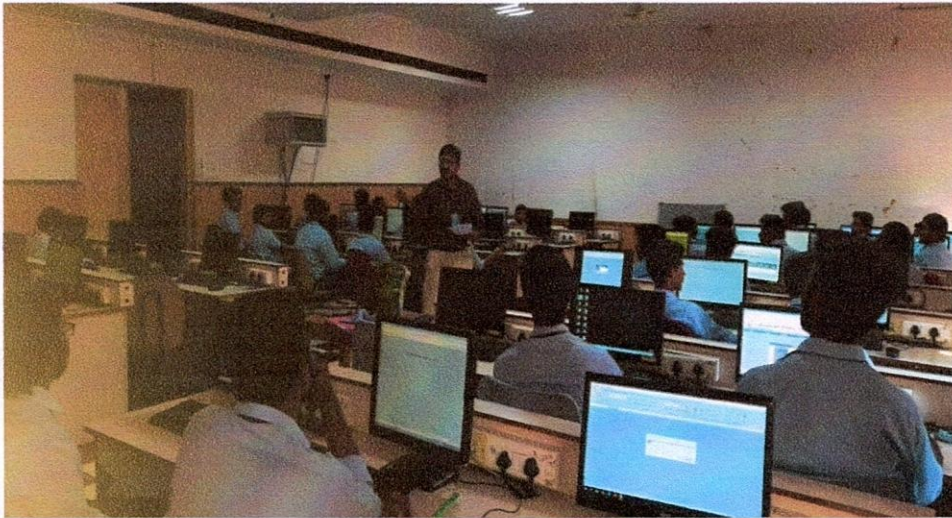
- To understand Siemens Digital Industries Software's products include NX, a CAD/CAM/CAE commercial software suite, Team centre, an integrated set of PLM and collaboration tools, Tecnomatix, a manufacturing and factory planning suite and Velocity Series, an application bundle focused at the mid-market that includes Solid Edge
- To enhance employability skill for Engineering Graduates in Research & Development technology implementation by imparting skills in Engineering Design in 3D Experience Platform, throughout for four year academic program.
- Students to implement innovative thoughts on siemens Experience platform for Product Development.
- Improve Students confidence levels by working in Domain's such as Aerospace, Automotive and off – shore Engineering.
- concept for the seamless integration of all information that arises in the course of the life cycle of a product.

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Outcome of Event:

- Student can acquire good knowledge on CAD/CAM/CAE.
- Siemens Digital Industries Software's products include NX, a CAD/CAM/CAE commercial software suite, Team centre, an integrated set of PLM and collaboration tools, Tecnomatix, a manufacturing and factory planning suite and Velocity Series, an application bundle focused at the mid-market that includes Solid_Edge
- Student can perform product designing, industrial designing and 2D design, drafting.
- concept for the seamless integration of all information that arises in the course of the life cycle of a product

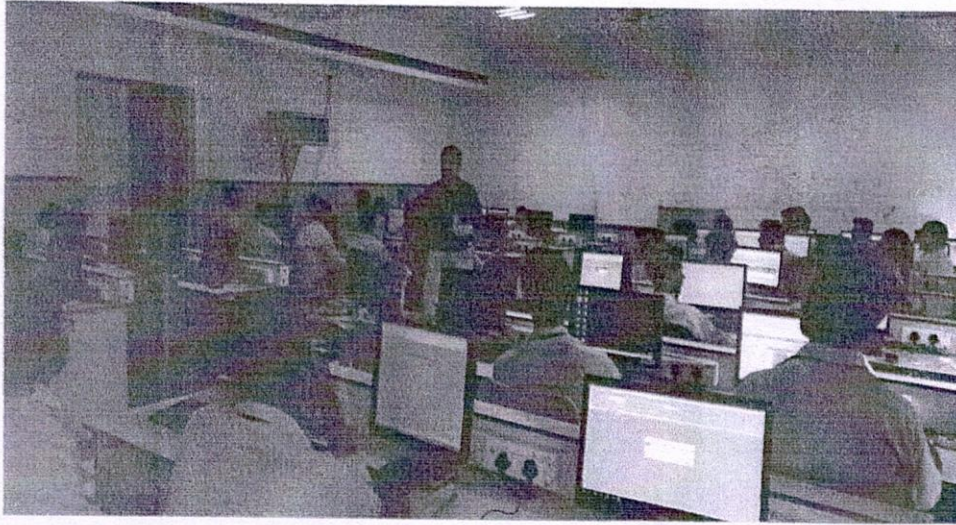

Program Coordinator


HOD/ Date


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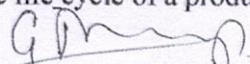



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Outcome of Event:

- Student can acquire good knowledge on CAD/CAM/CAE.
- Siemens Digital Industries Software's products include NX, a CAD/CAM/CAE commercial software suite, Team centre, an integrated set of PLM and collaboration tools, Tecnomatix, a manufacturing and factory planning suite and Velocity Series, an application bundle focused at the mid-market that includes Solid_Edge
- Student can perform product designing, industrial designing and 2D design, drafting.
- concept for the seamless integration of all information that arises in the course of the life cycle of a product


Program Coordinator


HOD/ Date


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Dt.06.06.2015

CERTIFICATE

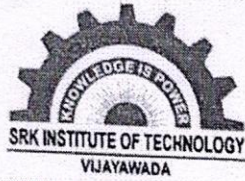
This is to certify that **Mr. NIMMAGADDA VAMSI KRISHNA (Reg.No.14X41E0053)**, MBA student of SRK Institute of Technology, Vijayawada affiliated to JNTUK has successfully completed the Finance Project work titled **"FUNDAMENTAL ANALYSIS OF SELECTED STOCKS IN IT SECTOR"** during 04.05.2015 to 06.06.2015. We found him to be hardworking and intelligent.

With regards,

T Siva Badrinath
Asst. Manager



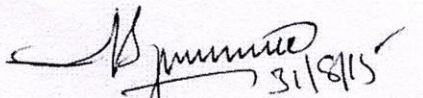
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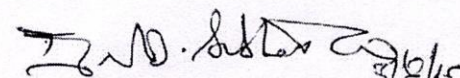


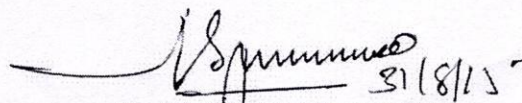
S.R.K. INSTITUTE OF TECHNOLOGY
DEPARTMENT OF BUSINESS ADMINISTRATION
(An ISO 9001-2008 Certified Institution, Approved by AICTE, Affiliated to JNTU -K)
Enikepadu, Vijayawada – 521 108

CERTIFICATE

This is to certify that the Mini project study entitled “**ETHICAL BEHAVIOR ON STOCK MARKETS WITH REFERENCE TO KARVY STOCK BROKING LIMITED, HYDERABAD**” is a bonafide work done by **N.VAMSI KRISHNA** Registered No. **14X41E0053** under my guidance and supervision in partial fulfillment of the requirement for the award of degree of **Master of Business Administration** (First Year admitted batch of 2014-15) in **S.R.K. Institute of Technology, Enikepadu, Vijayawada, affiliated to Jawaharlal Nehru Technological University, Kakinada**. This work is original and the same has not been submitted previously by any person for the degree of Master of Business Administration.


Dr.N.SUBRAMANYAM
Professor & Head


Mr. B.V.S.S.SUBBA RAO
Project review committee


Dr.N.SUBRAMANYAM
Professor & Head


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OPTIMUS

Commodity Futures Pvt Ltd.
(An ISO 9001 - 2008 Company)

22nd June, 2015

To
The Director/Principal,
S.R.K Institute of Technology
Vijayawada.

Sir,

Sub: Project Work Completion

We are pleased to inform you that your student Mr. VITAMSETTI SAI VENU MURALI KRISHNA with Regd No: 14X41E0034 of MBA has successfully Completed the Internship Project required for the curriculum of MBA Programme on the Topic "A COMPARATIVE STUDY ON PERFORMANCE OF NIFTY WITH MIDCAP" in our Organization for a duration from 04.05.2015 to 06.06.2015.

With Regards,

For OPTIMUS COMMODITY FUTURES PVT. LTD

M. S. Kumar
SIVA KUMAR MAJJI
HR Department



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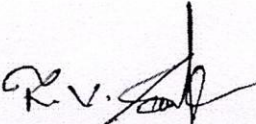
S.R.K. INSTITUTE OF TECHNOLOGY

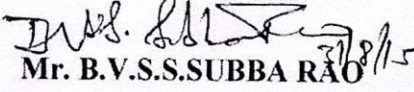
DEPARTMENT OF BUSINESS ADMINISTRATION

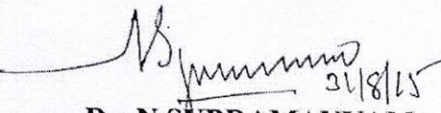
(An ISO 9001-2008 Certified Institution, Approved by AICTE, Affiliated to JNTU -K)


CERTIFICATE

This is to certify that the Mini project study entitled "A STUDY ON SEBI GUIDELINES TOWARDS STOCK EXCHANGE, INVESTORS" is a bonafide work done by **V.S.V.MURALI KRISHNA** Registered No. **14X41E0034** under my guidance and supervision in partial fulfilment of the requirement for the award of degree of **Master of Business Administration** (First Year admitted batch of 2014-15) in **S.R.K. Institute of Technology**, Enikepadu, Vijayawada, affiliated to **Jawaharlal Nehru Technological University**, Kakinada. This work is original and the same has not been submitted previously by any person for the degree of Master of Business Administration.


Mr. R.V. SANKARA RAO M.B.A.
Asst. Professor & Project Guide


Mr. B.V.S.S. SUBBA RAO
Project review committee


Dr. N. SUBRAMANYAM
Professor & Head of the department


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DEPARTMENT OF BUSINESS ADMINISTRATION

Date 30-10-2015

PROGRAM REPORT

Event Type	Faculty Development Program
Title of the Event	Derivatives Market
Event Start Date:	28/10/2015
Event End Date:	29/10/2015
No. of participants	27
Description	<p>A faculty development program was organized by the Department of Business Administration from 28th to 29th October, 2015 at Seminar hall on the above topic. This was a two day Faculty Development Program where various related subtopics were discussed in detail by eminent educationists. There are 27 participants were present in this programme and a very beneficial academic environment was experienced by the participants.</p> <p>The FDP was started on 28th October, 2015 with the prayer song. The dignitaries are welcomed by the Principal and opening remarks were also presented by him. The first technical session was on the 'topic Indian Financial System by the guest speaker by Mr. Zakir Hussain Director, Artha Financial Professional's Guru. He also discussed the different financial avenues available to invest. He also provided an insight on the types of financial markets. The afternoon session of day-1 was followed by lunch.</p> <p>On the second day, technical session 3 & 4 were also addressed by Mr. Zakir Hussain. The resource person enlightened the participants about features of exchange traded derivatives, classification and types of swaps and types of options and Different between Forwards and Futures, options and Swaps were discussed in detail over the two sessions of the day. In all, the technical sessions provided a good learning experience. The program was concluded with the valedictory note presented by Mr.J Sai Sudheer Kumar, Faculty, Department of Business Administration.</p>

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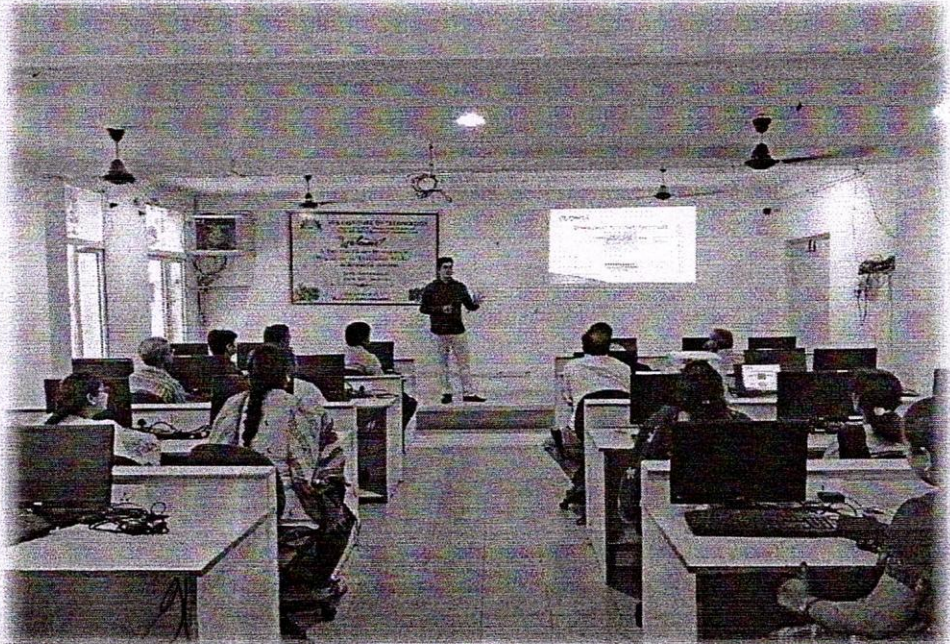
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DEPARTMENT OF BUSINESS ADMINISTRATION

The resource person explains the types of derivatives

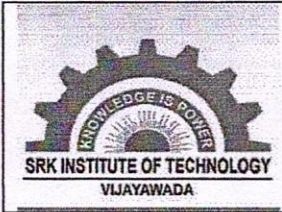


Resource person giving the example of Forward Contract



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The resource person effectively explaining the query raised by a participant



B. K. Reddy
COORDINATOR

[Signature]
HOD-MBA

[Signature]
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Department of Electronics and Communication Engineering
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Program Report

Date: 14-09-2015

Duration: 2 days (from 11-09-2015 to 12-09-2015)

The Departments of Electronics and Communication Engineering and CoreEL Technologies jointly organized a Two-day Faculty Development Program on **“Real Time Signal & Image Processing using Xilinx Vivado and Zynq- 7000 SOC”** from 11-09-2015 to 12-09-2015. The Resource Persons for the Session are Mr. K Ramesh Naidu, Zonal Manager, CoreEl Technologies, Mr. M Naga Raju, Application Engineer, CoreEl Technologies.

The Program was held in two sessions, (Forenoon) 9.00 am to 12.00 pm and (Afternoon) 1.00 pm to 5.00 pm for two days.

Number of Participants: 34



On day-1: Mr. K Ramesh Naidu gave an excellent session, on need of FPGAs/SOCs for addressing high performance DSP Designs, Introduction to Xilinx Vivado System Generator for DSP, Concepts of System modeling Using Simulink, Concepts of Hardware co- simulation using system Generator

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DSP, Getting started with simulink, Creating a 12x8 MAC Using the System Generator For DSP and Hardware co- simulation of FIR filter using Zynq Evaluation platform.



On day-2: Mr. K Ramesh Naidu, also gave an excellent session, on Addressing signal, video & image processing applications Using Xilinx FPGAs/SOCs, Real time video, Image , audio and signal processing applications, Converting MATLAB code into HDL and Implementation on ZedBoard, Real Time Image processing demo using ZedBoard and Real Time Video processing demo.



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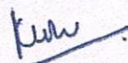
This faculty Development program is very useful technically for all the participants, they enjoyed the content, they learned so many new things.

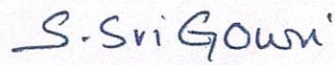
Course Objectives:

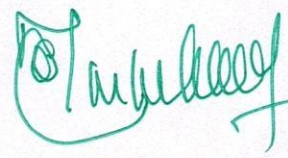
1. Understanding the need of FPGAs/SOCs for addressing high performance DSP Designs
2. Understanding the concepts of Hardware co- simulation using system Generator DSP
3. How to Create a 12x8 MAC Using the System Generator For DSP
4. Understanding Hardware co- simulation of FIR filter using Zynq Evaluation platform
5. Understanding Addressing signal, video & image processing applications Using Xilinx FPGAs/SOCs
6. Understanding Real time video, Image , audio and signal processing applications
7. How to Convert MATLAB code into HDL and Implementation on ZedBoard

Course Outcomes:

1. Understood the need of FPGAs/SOCs for addressing high performance DSP Designs
2. Understood the concepts of Hardware co- simulation using system Generator DSP
3. Understood Create a 12x8 MAC Using the System Generator For DSP
4. Understood Hardware co- simulation of FIR filter using Zynq Evaluation platform
5. Understood Addressing signal, video & image processing applications Using Xilinx FPGAs/SOCs
6. Understood Real time video, Image , audio and signal processing applications
7. Understood How to Convert MATLAB code into HDL and Implementation on ZedBoard


Co-ordinator


Head of the Department


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Email : SRKECEHOD@GMAIL.COM

1. Are you using Mentor Graphics Tools

- HEP-1 yes
 HEP-2yes

2. Which year you procured?

2015

3. Are you happy with support from CoreEL University Team?

yes

4. What are your current research challenges?

5. Ultra high speed design, to interconnect millions of gates which facilitate reuse and IP: portability

6. Did you Contacted CoreEL for Technical Support? If yes When and whom you contacted?

Yes, Mr M Nagaraju Nov15

7. Did CoreEL help you in solving your queries? How they helped?

Yes, some through telephone , some through training

8. Are you satisfied with Technical Support from CoreEL?

- Yes

9. Any feedback for improvement

No special feedback,

10. Are you interested in upgrading your existing software/Hardware? Whom should we contact?

Contact no and email id and name.

Dr S SRI GOWRI HOD ECE Dept. SRKIT Vijayawada 9866214098,srkecehod@gmail.com

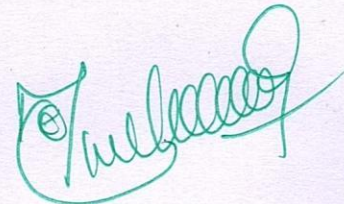
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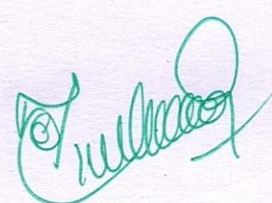
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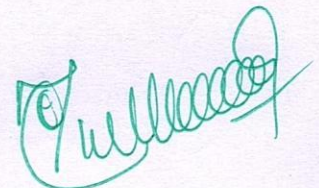

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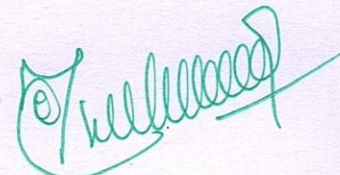
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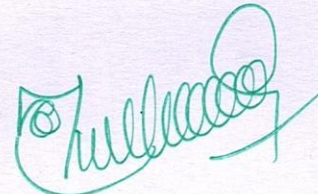


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4. What are your current research challenges? *Digital Image processing*
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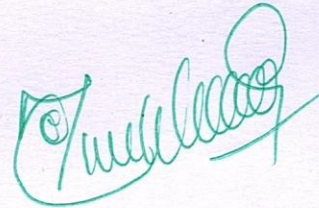
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2015
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4. What are your current research challenges? VLSI
5. Ultra high speed design, to interconnect millions of gates which facilitate reuse and IP: portability
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- HEP-1 yes
 HEP-2yes

2. Which year you procured?

2015 ✓

3. Are you happy with support from CoreEL University Team?

yes ✓

4. What are your current research challenges? *FINFET*

5. Ultra high speed design, to interconnect millions of gates which facilitate reuse and IP: portability

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Yes, Mr M Nagaraju Nov15

7. Did CoreEL help you in solving your queries? How they helped?

Yes, some through telephone , some through training

8. Are you satisfied with Technical Support from CoreEL?

Yes

9. Any feedback for improvement

No special feedback, *Two days workshop is insufficient, extra two more days is good.*

10. Are you interested in upgrading your existing software/Hardware? Whom should we contact?

Contact no and email id and name.

Dr S SRI GOWRI HOD ECE Dept. SRKIT Vijayawada 9866214098, srkecehod@gmail.com

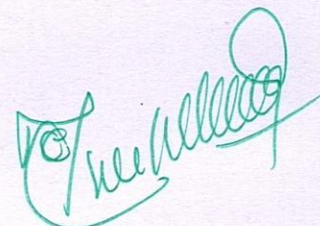
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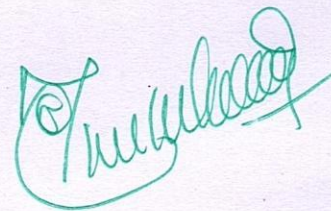

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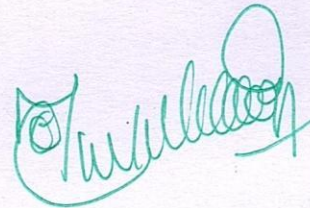
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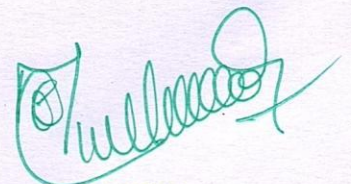
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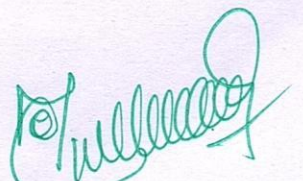


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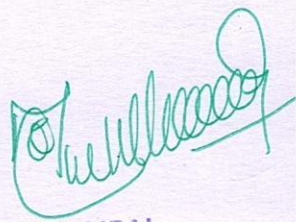

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ENIKEPADU, VIJAYAWADA-521 108



SRK INSTITUTE OF TECHNOLOGY

Enikepadu, Vijayawada 521108

Approved by AICTE, Affiliated to JNTUK, Kakinada

(ISO 9001:2008 Certified Institution)

Department of Computer Science and Engineering

Date: 26-03-2016

Report

Name of the Program: **“Java SE 6 Programmer Certified Professional”**.

Date: 14-03-2016 to 24-03-2016

Timings: 09:00 AM to 4:30 PM

The Department of CSE conducted program on **“Java SE 6 Programmer Certified Professional”**. Certification program from 14-03-2016 to 24-03-2016 at 2nd floor, BLC-4 during 09:00 AM to 4:30 PM

In total 26 students of III/II B.Tech attended the certification program. On the last day of the program few students gave their feedback on how the training sessions helped them in improving their programming skills.

Objective:

This course will make to

- Declarations, Initialization and Scoping
- Learned Flow Control methods with examples
- Write code that uses standard J2SE APIs in the java.util and java.util.regex packages to format or parse strings or streams.
- Develop code that implements tight encapsulation, loose coupling, and high cohesion in classes, and describe the benefits.
- The effect of modifiers on inheritance with respect to constructors, instance or static variables, and instance or static methods.
- Collections / Generics-Write code that uses the generic versions of the Collections API, in particular, the Set, List, and Map interfaces and implementation classes.
- Fundamentals-Given an example of a class and a command-line, determine the expected runtime behavior.

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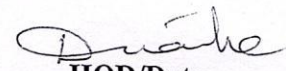
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Outcome of Event:

- The Oracle 1Z0-851 Certification Program certifies candidates on skills and knowledge related to Java products and technologies. The Oracle Certified Professional Java SE 6 Programmer is granted based on a combination of passing exams, training and performance-based assignments, depending on the level of certification. Oracle 1Z0-851 certification is tangible benchmark of experience and expertise that help you stand out in a crowd among employers.
- To ensure success, Oracle recommends combining education courses, practice exam and hands-on experience to prepare for your Java SE 6 Programmer Certified Professional certification exam as questions will test your ability to apply the knowledge you have gained in hands-on practice or professional experience.



Program Coordinator



HOD/Date



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