



BIST TECHNOLOGIES PRIVATE LIMITED

Door No: 33-21-4, First Floor, Seetharampuram Signals,
Eluru Road, Vijayawada -520002.

Memorandum of Understanding (MOU)

Between

BIST Technologies Pvt. Ltd

&

SRK Institute of Technology

Purpose

This Memorandum of Understanding (MOU) outlines the collaboration between BIST Technologies Pvt.Ltd, CIN- U62099AP2023PTC111817, Seetharampuram, Vijayawada, NTR District, Andhra Pradesh, represented by its Director having their registered office at Vijayawada and SRK Institute of Technology, Enikepadu, Vijayawada, NTR District, Andhra Pradesh 521108, represented by the Principal of the institution to provide exceptional opportunity tailored specifically for the students of your esteemed institution. We are offering industrial internships designed to provide practical insights and hands-on experience in cutting-edge domains for the ongoing R-20 and coming R-23 Regulation to Bridge the gap between Industry and Institution.

Benefits

The parties contemplate that they will benefit from this alliance as follows:

Benefits to SRK Institute of Technology

- We provide opportunity to the students to engage with the industry on a regular basis to understand the needs of the industry and accordingly update the curriculum.


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- Possibility of offering an advanced skill course for their current curriculum.
- We provide opportunity to collaborate with the industry for
 - a. Student Development Programs (SDP)
 - b. Projects
 - c. Faculty Development Programs(FDP)
 - d. Research programs
 - e. Student internships in the emerging fields of the industry
- We help students for the improvement of their employability skills.

Benefits to the student intern opting for offline internship program

- On a daily basis We will provide a 2hr theory offline session.
- Followed by a 2hr lab session.
- Tools relating to the domain they opt for will be provided to the interns.
- A one-hour communication skills session will be provided for the interns.
- An everyday assignment will be given to the interns.
- During their internship tenure, 2 assessments will be conducted.
- By the end of internship, we will provide
 - Internship completion certificate from APSCH
 - Certificate from BIST Technologies Pvt. Ltd
 - Merit certificate (for good performance in assessments).

Scope

BIST Technologies Pvt. Ltd agrees to provide internship opportunities for SRK Institute of Technology students in the following domains:

1. Machine Learning
2. Artificial Intelligence
3. Data Science


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4. Java Programming
5. Python Programming
6. OpenCV with Python
7. EV Design
8. VLSI Design for Testability
9. VLSI Front end RTL Design
10. Robotics Process Automation
11. Internet of Things (IoT)
12. Embedded Systems
13. Embedded C & C++
14. Mechatronics

Terms and Conditions:

1. At BIST Technologies Pvt. Ltd, we offer internships to SRK Institute of Technology students based on the Domain they opt for.
2. The duration of each internship will be mutually agreed upon by BIST Technologies Pvt. Ltd and SRK Institute of Technology, with a minimum duration of 6 weeks for the Short-Term and not less than 14 weeks for the Long-Term Internship, and Certification will be Provided as per the Requirement of Management of SRK Institute of Technology.
3. SRK Institute of Technology will facilitate the eligible students by coordinating with BIST Technologies Pvt. Ltd for the internships.
4. BIST Technologies Pvt. Ltd will provide necessary resources, guidance, and mentorship to the interns during their tenure at the company


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5. Interns will be required to abide by the rules, regulations, and confidentiality agreements of BIST Technologies Pvt. Ltd during their internship period.
6. Both parties agree to maintain regular communication, Assessments and provide feedback to ensure a mutually beneficial internship experience.

WHY BIST TECHNOLOGIES PVT. LTD?

BIST Technologies Pvt. Ltd is industry driven state-of-the-art training institute of excellence in various technologies such as VLSI DFT, Embedded systems, IOT, EV Design, Artificial Intelligence, Machine Learning, Data Science etc., Since our inception, approximately 10,000 students were trained with certification. We are the only company that is providing the students with both live online sessions & offline classes. Recorded classes will also be provided in our website.

We are approved by **APSCHE, AICTE & MSME**

Effectiveness

This MOU shall become effective upon signature by authorized representatives of both BIST Technologies Pvt. Ltd and SRK Institute of Technology.

Termination

This MOU may be terminated by either party with a notice of 30 days.

BIST Technologies Pvt. Ltd

Director: *R. Upendra Rao*

Signature: *R. Rao*

Date: *01/08/2023*



SRK Institute of Technology

Principal: *[Signature]*

Signature: *[Signature]*

Date: *01/08/2023*

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Approved by AICTE, Permanently Affiliated to JNTUK, Kakinada
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Date: 20-12-2023

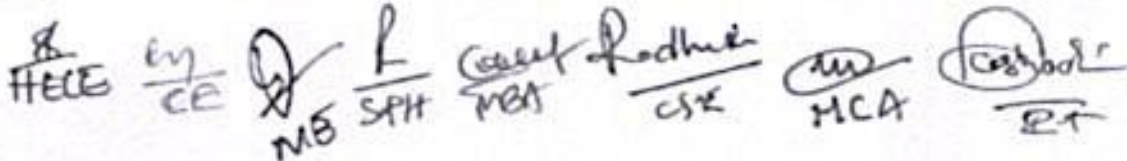
CIRCULAR


All the IV/IV B. Tech students are here by informed that Department of Electronics and Communication Engineering is offering a long term-Internship program on "VLSI Design for Testability". The program is to be held from 18-03-2024 to 25-04-2024. The details of the program are displayed in the notice board. All students are instructed to contact Ms. S. L. V. S. Jyothi, Assistant Professor, Department of Electronics and Communication Engineering on or before 15-01-2024. All the registered students must attend the program and complete the course.


Principal

Circulated to:

All HoDs




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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VLSI Design for Testability

S.No.	Roll.No.	Name of the Student	Internship
1	20X41A0401	AMBATI MEGHA VARDHAN	VLSI
2	20X41A0403	BHOGADI GOPIKA NAVYA HARSHITHA	VLSI
3	20X41A0404	BANDI MOUNIKA	VLSI
4	20X41A0405	BHIMIREDDY LAKSHMI SAMANVITHA	VLSI
5	20X41A0410	CHINNAM ASISH	VLSI
6	20X41A0412	DASARI SONIYA	VLSI
7	20X41A0413	DASARI VAMSI KRISHNA	VLSI
8	20X41A0415	DEVINENI SAI KUMAR	VLSI
9	20X41A0418	GOLTHI HEMA RAMESH	VLSI
10	20X41A0423	JALIPARTHI SIVA KARTHIK	VLSI
11	20X41A0424	JETTI MANIKANTA	VLSI
12	20X41A0425	KOMMINENI DIMPUL TEJA	VLSI
13	20X41A0427	KADALI SAI SATISH	VLSI
14	20X41A0428	KARANKI MAHESH	VLSI
15	20X41A0431	KODALI SUSHMA	VLSI
16	20X41A0432	KODATI NAGA SATYA SAI PRAKASH	VLSI
17	20X41A0434	KUMARAPU TARUN	VLSI
18	20X41A0437	MEKALA SRIKANTH REDDY	VLSI
19	20X41A0438	MOHAMMAD AYAZ	VLSI
20	20X41A0439	MOHAMMAD EDAYATULLA	VLSI
21	20X41A0444	NARRA RUPESH	VLSI
22	20X41A0447	NUNNA MADHU	VLSI
23	20X41A0455	SANGATI VISWA TEJA	VLSI
24	20X41A0459	SOMISETTY YASHWANTH	VLSI
25	20X41A0460	TUMMALAGUNTA DIVYA SRI	VLSI
26	21X45A0406	PAMULA DEENA	VLSI
27	21X45A0407	TEKI BHAGYESH	VLSI
28	21X45A0408	PINNAMANENI VENKAT RATNAM	VLSI
29	21X45A0409	K HARSHA NITHIN	VLSI
30	20X41A0465	B.J.B.N.V.S.SANDEEP	VLSI
31	20X41A0466	BATTULA PRAVEEN	VLSI
32	20X41A0471	DONTHALA.PAVANI DURGA	VLSI
33	20X41A0473	EDA PRAVALLIKA	VLSI
34	20X41A0474	GUDIVAKA NAGA PRAVEEN	VLSI
35	20X41A0478	GUNTAMUKKALA MARY DEEKSHITHA	VLSI
36	20X41A0479	JAMI ARAVIND KUMAR	VLSI

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37	20X41A0485	KURRA THANURYA	VLSI
38	20X41A0486	KURUVA SRAVANI	VLSI
39	20X41A0487	MANTADA MEGHANA	VLSI
40	20X41A0493	PULI SRIHARI	VLSI
41	20X41A0497	RUSHIKA PHANI PENNERU	VLSI
42	20X41A0498	SIMHADRI SAI KAMAL	VLSI
43	20X41A04A2	SHAIK SUHAIB AHMED	VLSI
44	20X41A04B1	TADISETTY KARTHIK SHANKAR	VLSI
45	21X45A0418	TIRIVEEDHI LEKHA SREE	VLSI
46	21X45A0419	THUPAKULA DURGA PRASAD RAO	VLSI
47	21X45A0424	SHAIK JOEL SHONE	VLSI


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02-05-2024

PROGRAM REPORT

- Name of the program** : Long Term Internship on "VLSI Design for Testability"
- Date/Duration** : 18-03-2024 to 25-04-2024 (240 hours)
- Resource Person** : Mr. Sk. Ashraf Ali
- Name of the Coordinator** : Ms. S. L. V. S. Jyothi, Assistant Professor, ECE.
- Number of Participants** : 47
- Program Objectives** : Students learn VLSI (Very Large Scale Integration) Design for Testability (DFT) both theoretical and practical development in the field of semiconductor design and testing by understand the fundamental principles and concepts of Design for Testability, and DFT architectures using various testing techniques and methodologies used in VLSI design, built-in self-test (BIST), boundary scan (JTAG) techniques in VLSI designs using industry-standard Electronic Design Automation (EDA) tools.
- Topics Covered** :
1. Introduction to VLSI
 2. Introduction to VLSI Testing
 3. Introduction to BIST
 4. BIST Architectures
 5. Linear feedback shift registers (LFSRs)
 6. Boundary Scan (JTAG)
- Program Outcomes** : By completion of the internship students can be highly beneficial, providing them with both theoretical knowledge and practical skills relevant to the field of semiconductor design and testing. Here are some potential outcomes for students studying VLSI Design for Testability of the principles and concepts of Design for Testability, including fault models, testability metrics, and DFT architectures


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This is to Certify that Ms./ Mr. CHINNAMASISH Course: B.TECH Branch: FCE Sem: 8
Roll/Reg No: 20X41A0410 under SRK Institute of Technology of INTUK has Successfully
Completed Long-Term Internship for 240 hours on VLSI Design For Testability Organized by
BIST TECHNOLOGIES PVT.LTD in Collaboration with the Andhra Pradesh State Council of
Higher Education.

Date: **25/04/2024**
Place: **Vijayawada**



R. Upendar Rao
Director

R. Upendar Rao
DIN: 10252403

PRINCIPAL
SRK INSTITUTE OF TECHNOLOGY
ENIKEPADU, VIJAYAWADA-521 10B.



ANDHRA PRADESH STATE COUNCIL OF HIGHER EDUCATION

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CERTIFICATE OF COMPLETION

This is to Certify that Ms./ Mr. BHIMIREDDY LAKSHMI SAMANVITHA Course: B.Tech. Branch: ECE Sem: 8th Roll/Reg No: 20X41A0405 under SRK Institute of Technology of INTUK has Successfully Completed Long-Term Internship for 240 hours on VLSI Design For Testability Organized by BIST TECHNOLOGIES PVT.LTD in Collaboration with Andhra Pradesh State Council of Higher Education.

Date 08/04/2024
Place, Vijayawada



RuRao
Director

R. Upendar Rao
DIN: 10252403

[Handwritten Signature]

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This is to Certify that Ms./ Mr. BANDIMOUNIKA Course: B.TECH Branch: ECE Sem:8
Roll/Reg No: 20X41A0404 under SRK Institute of Technology of INTUK has Successfully
Completed Long-Term Internship for 240 hours on VLSI Design For Testability Organized by
BIST TECHNOLOGIES PVT. LTD in Collaboration with the Andhra Pradesh State Council of
Higher Education.

Date: 25/04/2024
Place: Vijayawada



R. Upendar Rao
Director

R. Upendar Rao
DIN: 10252403

(Signature)

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ANDHRA PRADESH STATE COUNCIL OF HIGHER EDUCATION

16, Station Road, Hyderabad - 500 002, Andhra Pradesh



This is to Certify that Ms. / Mr. Bhogadi Gopika Navya Harshitha Course: B. TECH Branch: ECE Sem: 8 Roll/Reg No: 20X41A0403 under SRK Institute of Technology of INTUK has Successfully Completed Long-Term Internship for 240 hours on VLSI Design For Testability Organized by BIST TECHNOLOGIES PVT. LTD in Collaboration with the Andhra Pradesh State Council of Higher Education.

Date: **25/04/2024**
Place: **Vijayawada**



RyPoo
Director

R. Upendar Rao
DIN:10252403

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This is to Certify that Ms./ Mr. Ambati Megha Vardhan Course: B. TECH Branch: ECE
Sem: 8th Roll/Reg No: 20X41A0401 under SRK Institute of Technology of JNTUK has
Successfully Completed Long-Term Internship for 240 hours on VLSI Design For Testability
Organized by BIST TECHNOLOGIES PVT. LTD in Collaboration with the Andhra Pradesh
State Council of Higher Education.

Date: 22/04/2024
Place: Vijayawada



R. Upendar Rao
Director

R. Upendar Rao
DIN:10252403

(Signature)

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Date: 20-12-2023

CIRCULAR

All the IV/IV B. Tech students are hereby informed that Department of Electronics and Communication Engineering is offering a long term Internship program on "Internet Of Things". The program is to be held from 18-03-2024 to 25-04-2024. The details of the program are displayed in the notice board. All students are instructed to contact Ms. A. Ramya, Assistant Professor, Department of Electronics and Communication Engineering on or before 15-01-2024. All the registered students must attend the program and complete the course.

Principal

Circulated to:

All HoDs $\frac{8}{HECE}$ $\frac{by}{CE}$ $\frac{Jadhav}{CSE}$ $\frac{D}{ME}$ $\frac{R}{SPH}$ $\frac{Gangotri}{MBA}$ $\frac{Anu}{MCA}$ $\frac{Kishorek}{Z-1}$

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Internet of Things

S.No.	RollNo.	Name of the Student	Internship
1	20X41A0468	BITRA ROOPA SRI	IOT
2	20X41A0469	CHAVALI TANUJ	IOT
3	20X41A0470	DAGGULA VENKATA REDDY	IOT
4	20X41A0476	GAJJALAKONDA RAMYA	IOT
5	20X41A0481	KANURI LAKSHMI CHANDRIKA	IOT
6	20X41A0484	KOTTAPALLI VENKATESH	IOT
7	20X41A0488	MOHAMMAD MINHAJ BAIG	IOT
8	20X41A0489	MUVVA SRI SWATHI	IOT
9	20X41A0491	PARASU PRAVEEN GANDHI	IOT
10	20X41A0496	REDROTHU SRAVYA	IOT
11	20X41A04A6	VANAPAMULA VENKATA SIVA	IOT
12	20X41A04A8	VUKKADAPU NAGA SAI	IOT
13	20X41A04A9	YADLA SUNAGA SRI	IOT
14	21X45A0412	EPURI ROHITH	IOT
15	21X45A0414	SARVISETTY NAGA DURGA BABU	IOT
16	21X45A0415	BANKA UMA MAHESWARA RAO	IOT
17	21X45A0416	BANALA SAI NEERAJ	IOT

H/D

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02-05-2024

PROGRAM REPORT

- Name of the program** : Long Term Internship on "Internet of Things"
- Date/Duration** : 18-03-2024 to 25-04-2024 (240 hours)
- Resource Person** : Mr. P. Rajesh
- Name of the Coordinator** : Ms. A. Ramya, Assistant Professor, ECE.
- Number of Participants** : 17
- Program Objectives** : Students learn both theoretical knowledge and practical skills to provide them with a comprehensive understanding on Internet of things about sensors, actuators, connectivity, data analytics, and with various real-world applications of IoT across different domains such as healthcare, agriculture, smart cities, transportation, and manufacturing.
- Topics Covered** :
1. Introduction to IoT
2. IoT Architecture
3. IoT Devices and Technologies
4. IoT Communication Protocols
5. IoT Development Platform and Tools
6. Security and Privacy in IoT
- Program Outcomes** : By completion of the internship students can have a significant impact on their academic, professional, and personal development to gain a comprehensive understanding of IoT concepts, principles, and technologies. Develop hands-on skills in designing, building, and deploying IoT systems and applications using hardware components, microcontrollers


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CERTIFICATE OF COMPLETION

This is to Certify that Ms./ Mr. GAIJALAKONDA RAMYA Course: B. TECH Branch: ECE Sem: 8th Roll/Reg No: 20X41A0476 under SRK Institute of Technology of JNTUK has Successfully Completed Long-Term Internship for 240 hours on Internet of Things Organized by BIST TECHNOLOGIES PVT. LTD in Collaboration with the Andhra Pradesh State Council of Higher Education.

Date: 22/04/2024
Place: Vijayawada



R. Upendar Rao

R. Upendar Rao
DIN: 10252403


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CERTIFICATE OF COMPLETION

This is to Certify that Ms./ Mr. Daggula venkata Reddy Course: B. TECH Branch: ECE
Sem: 8th Roll/Reg No: 20X41A0470 under SRK Institute of Technology of INTUK has
Successfully Completed Long-Term Internship for 240 hours on Internet of Things Organized
by BIST TECHNOLOGIES PVT. LTD in Collaboration with the Andhra Pradesh State Council
of Higher Education.

Date: 22/04/2024
Place: Vijayawada



RuPoo
Director

R. Upendar Rao
DIN:10252403


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This is to Certify that Ms./ Mr. CHAVALI TANUJ Course: B.Tech. Branch; ECE Sem; 8th
Roll/Reg No: 20X41A0469 under SRK Institute of Technology of INTUK has
Successfully Completed Long-Term Internship for 240 hours on Internet of Things
Organized by BIST TECHNOLOGIES PVT.LTD. in Collaboration with Andhra Pradesh
State Council of Higher Education.

Date 08/04/2024
Place: Vijayawada



R. Upendar Rao
Director

R. Upendar Rao
DIN: 10252403


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CERTIFICATE OF COMPLETION

This is to Certify that Ms./ Mr. BITRA ROOPA SRL Course: B.Tech. Branch: ECE. Sem: 8th Roll/Reg No: 20X71A0468 under SRK Institute of Technology of INTUK has Successfully Completed Long-Term Internship for 240 hours on Internet of Things Organized by BIST TECHNOLOGIES PVT.LTD. in Collaboration with Andhra Pradesh State Council of Higher Education.

Date: 08/04/2024
Place: Vijayawada



R. Upendar Rao
Director
R. Upendar Rao
DIN: 10252403

[Signature]
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CERTIFICATE OF COMPLETION

This is to Certify that Ms./ Mr. KANURLAKSHMI CHANDRIKA Course: B.Tech. Branch; ECE_Sem; 8th_Roll/Reg No: 20X41A0481 under SRK Institute of Technology of INTUK has Successfully Completed Long-Term Internship for 240 hours on Internet of Things Organized by BIST TECHNOLOGIES PVT.LTD. in Collaboration with Andhra Pradesh State Council of Higher Education.

Date 08/04/2024
Place: Vijayawada



Ry Rao
Director

R.Upendar Rao
DIN:10252403

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Date: 20-12-2023

CIRCULAR

All the IV/IV B. Tech students are hereby informed that Department of Electronics and Communication Engineering is offering a long term Internship program on "Python Programming". The program to be held from 18-03-2024 to 25-04-2024. The details of the program are displayed in the notice board. All students are instructed to contact Dr. T. Vishnu Priya, Associate Professor, Department of Electronics and Communication Engineering on or before 15-01-2024. All the registered students must attend the program and complete the course.

Principal

Circulated to:

All HoDs

HECE SEH MCA CE Roshika MCA Prasad MB

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Python Programming

S.No.	Roll.No.	Name of the Student	Internship
1	20X41A0462	VULLI ANIL	PYTHON
2	20X41A0463	VENKAIAH CHOWDARY BIKKI	PYTHON
3	20X41A0464	YADDANAPUDI VENKATESH	PYTHON
4	21X45A0423	KOLLATHUVALAPIL SAI KRISHNA	PYTHON


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02-05-2024

PROGRAM REPORT

- Name of the program** : Long Term Internship on "Python Programming"
- Date/Duration** : 18-03-2024 to 25-04-2024 (240 hours)
- Resource Person** : Mr. B. Radha
- Name of the Coordinator** : Dr. T. Vishnu Priya, Associate Professor, ECE.
- Number of Participants** : 4
- Program Objectives** : Students learn to provide with a solid foundation in Python programming, enabling them to apply their skills in various domains and prepare them for further learning and career opportunities in the field of computer science and technology problem-solving skills by teaching them how to break down problems into smaller, manageable tasks.
- Topics Covered** :
1. Introduction to Python
2. Functions
3. Modules
4. Data Structures
5. File Handling
6. Network Programming
- Program Outcomes** : By completion of the internship students will have a profound impact on their academic, professional, and personal development empowers students with the knowledge, skills, and confidence to excel in their academic pursuits, pursue career opportunities, and contribute meaningfully to the digital world. Experience personal enrichment and satisfaction from the ability to create, innovate, and solve problems using the versatile and powerful Python programming language


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CERTIFICATE OF COMPLETION

This is to Certify that Ms./ Mr. YADDANAPUDI VENKATESH Course: B.Tech. Branch;
ECE_Sem;_8th_Roll/Reg No: 20X41A0464 under SRK Institute of Technology of
INTUK has Successfully Completed Long-Term Internship for 240 hours on Python
Programming Organized by BIST TECHNOLOGIES PVT.LTD. in Collaboration with
Andhra Pradesh State Council of Higher Education.

Date 08/04/2024
Place: Vijayawada



Ry Rao
Director

R. Upendar Rao
DIN: 10252403

PRINCIPAL
SRK INSTITUTE OF TECHNOLOGY
ENIKEPADU, VIJAYAWADA-521 108.



ANDHRA PRADESH STATE COUNCIL OF HIGHER EDUCATION

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CERTIFICATE OF COMPLETION

This is to Certify that Ms./ Mr. VENKAIAH CHOWDARY BIKKI Course: B.Tech.
Branch: ECE Sem: 8th Roll/Reg No: 20X41A0463 under SRK Institute of Technology
of INTUK has Successfully Completed Long-Term Internship for 240 hours on
Python Programming Organized by BIST TECHNOLOGIES PVT.LTD. in
Collaboration with Andhra Pradesh State Council of Higher Education.

Date 08/04/2024
Place: Vijayawada



R. Upendar Rao
Director

R. Upendar Rao
DIN: 10252403

[Handwritten Signature]
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CERTIFICATE OF COMPLETION

This is to Certify that Ms./ Mr. VULLANIL Course: B.Tech. Branch: ECE Sem: 8th
Roll/Reg No: 20X41A0462 under SRK Institute of Technology of INTUK has
Successfully Completed Long-Term Internship for 240 hours on Python
Programming Organized by BIST TECHNOLOGIES PVT.LTD. in Collaboration with
Andhra Pradesh State Council of Higher Education.

Date: 08/04/2024
Place: Vijayawada



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CERTIFICATE OF COMPLETION

This is to Certify that Mr. RAAVI AKHIL Course: B.Tech. Branch: ECE Sem: 8th
Roll/Reg No: 20X1A01015 under SRK Institute of Technology of INTUK has
 Successfully Completed Long-Term Internship for 240 hours on Python
Programming Organized by BIST TECHNOLOGIES PVT.LTD. in Collaboration with
Andhra Pradesh State Council of Higher Education.

Date 08/04/2024
 Place: Vijayawada

R. Upendar Rao
 Director

R. Upendar Rao
 DIN: 10252403



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CERTIFICATE OF COMPLETION

That Ms./ Mr. KOLLATHU VALAPIL SAI KRISHNA Course: B.Tech
8th Roll/Reg No: 21X/5A0423 under SRK Institute of Technology
successfully Completed Long-Term Internship for 240 hours on
ning Organized by BIST TECHNOLOGIES PVT.LTD. in
Andhra Pradesh State Council of Higher Education.

R. Upendar Rao
Director



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